



PERIYAR UNIVERSITY

PERIYARPALKALAINAGAR

SALEM-636011

DEGREE OF BACHELOR OF SCIENCE

OUTCOME BASED EDUCATION

Syllabus for

B.SC.ELECTRONICS AND COMMUNICATION

(SEMESTER PATTERN)

(For Candidates admitted in the Colleges affiliated to Periyar University from 2021-2022 onwards)

Programme Educational Objectives (PEOs):

Programme Educational Objectives (PEOs) are established through a consultation process. PEOs are broad statements that describe the career and professional accomplishments that the graduates can find opportunities in Sales, Marketing, Service and R&D Sectors.

The B.Sc. Degree programme graduates will.....

- **PEO 1** Practice the ethics of their profession consistent with a sense of social responsibility and develop their engineering design, problem – solving skills and aptitude for innovations as they work individually and in multi-disciplinary teams.
- **PEO 2** Communicate effectively and manage resources skillfully as members and leaders of the profession.
- **PEO 3** Be receptive to new technologies and attain professional competence through lifelong learning such as advanced degrees, professional registration, publications and other professional activities.

Programme Specific Outcomes:

On completion of the B.Sc Degree in Electronics and Communication, graduates will be able to

- **PSO 1** Apply the fundamental concepts of electronics and communication to design a variety of components and systems for applications including communication, networking, embedded systems, PCB, Mobile, TV and etc.
- **PSO 2** Select and apply cutting-edge hardware and software tools to solve complex Electronics and Communication problems.

Programme Outcomes:

On completion of the B.Sc. degree the Electronics and Communication graduates will be able to

- **PO 1** Utilize the basic knowledge in mathematics, science in Electronics and Communication areas.
- **PO 2** Identify, formulate and solve complex problems to achieve demonstrated conclusions using mathematical principles.
- **PO 3** Design system components that meet the requirement of public safety and offers solutions.
- **PO 4** Apply research-based knowledge to design and conduct experiments, analyze, synthesize and interpret the data pertaining to Electronics and arrive at valid conclusions.
- **PO 5** Construct, choose and apply the techniques, resources and modern tools required for Electronics applications.
- **PO 6** Examine the impact of Electronics solutions in global and environmental contexts and utilize the knowledge for sustained development.
- **PO 7** Develop consciousness of professional, ethical and social responsibilities as experts in the field of Electronics.

REGULATIONS

1. ELIGIBILITY:

Candidates seeking admission to the first year of the Bachelor of Science in Electronics & Communications should have passed the Higher Secondary Examination conducted by the Government of Tamil Nadu or an examination accepted as Equivalent there to by the Syndicate subject to such conditions as may be prescribed from time to time are permitted to appear and qualify for the B.Sc., Electronics and Communication Degree of this university after a course of study of Three Academic Years.

2. DURATION OF THE COURSE:

The course for the degree of Bachelor of Electronics and Communication shall consist of three academic years divided into six semesters. Each semester will be of 90 working days.

3. COURSE OF STUDY:

The course of study shall comprise instruction in the following subjects under OBE (Outcome Based Education) pattern according to the syllabus and books prescribed from time to time.

FOUNDATIONS SUBJECTS:

PART I: Tamil/Hindi/Malayalam/French/German

ART II: English

ALLIED SUBJECTS: IN THE FIRST YEAR ON A MAJOR

Department out of the four mentioned below; to be chosen for the study of two allied papers. In the second year another major department in the list to be chosen for the study of another two allied papers.

| SEMESTER | MAJOR DEPARTMENT | ALLIED SUBJECT |
|----------|------------------|---|
| I/III | MATHS | ALGEBRA, CALCULUS & FOURIER SERIES |
| I/III | PHYSICS | ALLIED PHYSICS - I |
| I/III | COMPUTER SCIENCE | PROGRAMMING IN C |
| II/IV | MATHS | DIFFERENTIAL EQUATIONS AND LAPLACE TRANSFORMS |
| II/IV | PHYSICS | ALLIED PHYSICS - II |
| II/IV | COMPUTER SCIENCE | PROGRAMMING IN VISUAL BASIC |
| II | | ALLIED LAB - I FOR ALLIED I & II MATHS/PHYSICS /C & VB LAB |
| IV | | ALLIED LAB - II FOR ALLIED III & IV MATHS/PHYSICS /C & VB LAB |

5. SCHEME OF EXAMINATIONS

The scheme of examinations under OBE (Outcome Based Education) for different semesters shall be as follows.

| SEMESTER I | | | | | | | | |
|-------------|---|---|-------|-------|----------|-------|----|-------|
| PART | SUBJECT | SUBJECT TITLE | TOTAL | HOURS | CRE-DITS | TOTAL | | |
| | | | L | T/P | | CIA | EA | MARKS |
| I | Language | Tamil-I@ | 6 | 2 | 3 | 25 | 75 | 100 |
| II | Language | English – I COMMUNICATIVE ENGLISH | 6 | | 3 | 25 | 75 | 100 |
| III | Core-I | Semiconductor Devices | 4 | | 4 | 25 | 75 | 100 |
| III | Core Pract.-I | Basic Electronics Lab | 1 | | | | | |
| III | Allied I | | 4 | | 4 | 25 | 75 | 100 |
| III | Allied Lab-I | | 2 | | | | | |
| IV | SBEC-I | Applied Electric Circuits | 2 | | 2 | 25 | 75 | 100 |
| IV | Value Education | Yoga | 1 | | 2 | 25 | 75 | 100 |
| IV | Add-on | Professional English-I | 2 | | 4 | 25 | 75 | 100 |
| SEMESTER II | | | | | | | | |
| PART | SUBJECT | SUBJECT TITLE | TOTAL | HOURS | CRE-DITS | TOTAL | | |
| | | | L | T/P | | CIA | EA | MARKS |
| I | Language | Tamil -II@ | 6 | | 3 | 25 | 75 | 100 |
| II | Language | English– II COMMUNICATIVE ENGLISH | 4 | | 3 | 25 | 75 | 100 |
| II | Naan Muthalvan Skill Development Course | Language Proficiency for Employability- Effective English | 2 | | 2 | 25 | 75 | 100 |
| III | Core- II | Applied Digital Electronics | 4 | | 4 | 25 | 75 | 100 |
| III | Core Pract.-I | Basic Electronics Lab | 1 | 2 | 4 | 40 | 60 | 100 |
| III | Allied-II | | 4 | | 4 | 25 | 75 | 100 |

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| | | | | | | | | |
|------|-------------------|------------------------|---|---|---|----|----|-----|
| III | AlliedLab -I | | | 2 | 2 | 40 | 60 | 100 |
| IV | SBEC-II | PowerElectronics | 2 | | 2 | 25 | 75 | 100 |
| IVIV | EVS Add-on course | EnvironmentalStudies | 1 | | 2 | 25 | 75 | 100 |
| | | ProfessionalEnglish-II | 2 | | 4 | 25 | 75 | 100 |

SEMESTERIII

| PART | SUBJECT | SUBJECTTITLE | TOTAL | HOURS | CRE-DITS | TOTAL | | |
|------|----------------|--|-------|-------|----------|-------|----|-------|
| | | | L | T/P | | CIA | EA | MARKS |
| I | Language | Tamil-III@ | 6 | | 3 | 25 | 75 | 100 |
| II | Language | English-III | 6 | | 3 | 25 | 75 | 100 |
| III | CoreIII | ElectronicCircuits | 4 | | 4 | 25 | 75 | 100 |
| III | CorePract.-II | ElectronicCircuitsPractical | 1 | 2 | | | | |
| III | CorePract.-III | 8085Microprocessor | 1 | 2 | | | | |
| III | Allied III | | 4 | | 4 | 25 | 75 | 100 |
| III | AlliedLab-II | | 2 | | | | | |
| IV | NMSDCI | Digital Skills for Employability-Microsoft Office Essentials | 2 | | 2 | 25 | 75 | 100 |

SEMESTERIV

| PART | SUBJECT | SUBJECTTITLE | TOTAL | HOURS | CRE-DITS | TOTAL | | |
|------|-----------------|------------------------------------|-------|-------|----------|-------|----|-------|
| | | | L | T/P | | CIA | EA | MARKS |
| I | Language | Tamil- | 6 | | 3 | 25 | 75 | 100 |
| II | Language | IV@English- | 6 | | 3 | 25 | 75 | 100 |
| III | CoreIV | IV | 4 | | 4 | 25 | 75 | 100 |
| III | CorePract. II | 8085 Microprocessor andInterfacing | 1 | 2 | 4 | 40 | 60 | 100 |
| III | CorePract. III | ElectronicCircuits Lab | 1 | 2 | 4 | 40 | 60 | 100 |
| III | AlliedIV | 8085 | 4 | | 4 | 25 | 75 | 100 |
| III | Allied Lab - II | Microprocessor andInterfacingLab | | 2 | 2 | 40 | 60 | 100 |

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|----|-------------------------------------|--------------------------------|-------------------|-----------------|---|----|----|-----|
| IV | NaanMuthalvanSkillDevelopmentCourse | Employability Skills-Microsoft | 2 | - | 2 | 25 | 75 | 100 |
| IV | Add-oncourse | INTERNSHIP!* | Industry Oriented | Training 15days | | | | |

SEMESTER V

| PART | SUBJECT | SUBJECT TITLES | TOTAL | HOURS | CRE-DITS | TOTAL | | |
|------|---------------|---|-------|-------|----------|-------|----|-------|
| | | | L | T/P | | CIA | EA | MARKS |
| III | Core V | Electronic Communication Systems | 5 | | 5 | 25 | 75 | 100 |
| III | Core VI | IC's & Their Applications | 5 | | 5 | 25 | 75 | 100 |
| III | Elective I | From Group A | 5 | | 5 | 25 | 75 | 100 |
| III | Elective II | From Group B | 5 | | 4 | 25 | 75 | 100 |
| III | Core Pract-IV | IC'S & Communication Lab | 1 | 2 | | | | |
| III | Core Pract-V | From Group D | 1 | 2 | | | | |
| IV | SBEC III | Electronic Instrumentation | 2 | | 2 | 25 | 75 | 100 |
| IV | NMSDC | Computational Intelligence for Employability-Drone Pilot Techniques | 2 | | 2 | 25 | 75 | 100 |

SEMESTER VI

| PART | SUBJECT | SUBJECT TITLE | TOTAL | HOURS | CRE-DITS | TOTAL | | |
|------|-----------------|---|-------|-------|----------|-------|-----|-----|
| | | | L | T/P | | CIA | CIA | CIA |
| III | Core VII | PC Hard Ware Networking & Troubleshooting | 5 | | 5 | 25 | 75 | 100 |
| III | Core VIII | Network Communication & Security | 5 | | 5 | 25 | 75 | 100 |
| III | Elective III | From Group C | 5 | | 5 | 25 | 75 | 100 |
| III | Core Pract. I V | IC'S & Communication Lab | 1 | 2 | 4 | 40 | 60 | 100 |

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| | | | | | | | | |
|------------|----------------------|--|-----------|--------------|------------|-----------|-----------|-------------|
| III | Core Pract. V | From Group D | 1 | 2 | 4 | 40 | 60 | 100 |
| III | Core IX | Biomedical Instruments (or) Project Viva Voce* | 5 | | 5 | 25 | 75 | 100 |
| III | NMSDC | Advanced Data Analytics & Visualization | 2 | - | 2 | 25 | 75 | 100 |
| IV | SBEC VI | Life Development Skills | 2 | | 2 | 25 | 75 | 100 |
| V | Extension Activities | (NCC/NSS/YRC/Sports Etc.) | | | 1 | | | |
| | | Total Credits & Marks | | | 150 | | | 4100 |
| * | | No Internal Mark For Project Work | | | | | | |
| | | Project Report Evaluation | 80 | Marks | | | | |
| | | Viva Voce Evaluation | 20 | Marks | | | | |

!* Internship: To be carried out 4th semester vacation period, The internship course will not meet as a regular class.

@ Any other Language like Hindi/Malayalam/French/etc.

#\$ Those who have not studied Tamil upto XII std and taken a Non Tamil language under part-I shall take Tamil comprising of two courses (level will be at 6th standard) instead of NMEC

#\$ Those who have studied Tamil upto XII std and taken a Non Tamil language under part-I shall take Advanced Tamil comprising of two courses instead of NMEC.

5.A. INTERNSHIP OBJECTIVES

The internship could be a new job or a new experience within an existing job as the work performed in the internship should lead to new learning, discovery or growth for the student and contribute toward the student's academic program.

The student intern will either find a company to sponsor him or her or perform a specific project at an existing job. The internship program is an agreement between the student, faculty advisor and the company supervisor.

Perform job requirements in a professional manner.

Maintain regular and prompt attendance, as agreed upon with supervisor. The submission of report is written at the end of the internship by the student to the institute which form part in the awarding of the degree certificate.

INTERNSHIP OUTCOME

An internship provides the student the opportunity to build upon, apply, and assess the concepts that are developed through the College's curriculum and to further the student's professional growth through a meaningful "real-world" job experience. Internships.

6. QUESTION PAPER PATTERN FOR ALL UG COURSES

MARK DISTRIBUTION FOR THEORY (EXTERNAL)

TIME: 3 HOURS

MAXIMUM MARKS :

75 PASSING MINIMUM : 30 MARKS

Part A

15x1=15 (Answer All Questions)

(Three Questions from Each Unit)

Part B

5x2=10 (Answer Any Two Questions) (One Question from Each Unit)

Part C

5x10=50 (Answer All Questions)

MarkDistributionforTheory(Internal)

Max.Marks: 25

PassingMinimum:10Marks

| | | |
|-----------------------------------|----------|-----------|
| SubjectLevelCycleTestandModelExam | : | 15 |
| SubjectLevelAssignment | : | 5 |
| SubjectLevelAttendance | : | 5 |
| | | ----- |
| Total | : | 25 |
| | | ----- |

MARK DISTRIBUTION FOR PRACTICALS**(EXTERNAL)TIME:3HOURS;MAXIMUMMARKS:60;PASSINGMINIMUM:24M****ARKSPRACTICALEXAM:50MARKS&RECORD:10MARKS**

Submission of Record Note Books for Practical Exams: Candidates appearing for the Practical Exams must submit Bonafide Record Note Book, otherwise the candidate will not be permitted to appear for the practical exam.

MarkDistributionforPractical(Internal)

Max.Marks:40

PassingMinimum:16Marks

| | | |
|----------------------------|----------|-----------|
| YearThrough;LabPerformance | : | 10 |
| ModelPractical's | : | 20 |
| YearThrough;LabAttendance | : | 10 |
| | | ----- |
| Total | : | 40 |

7. PROJECT WORK OR ONE CORE PAPER:

A candidate can submit

SOFTWARE or HARDWARE or HARDWARE cum SOFTWARE based project and has to demonstrate the Project with Project Report in the University Project Viva Voce Examination conducted at the end of the sixth semester.

MARK DISTRIBUTION FOR PROJECT REPORT: 80 MARKS

MARK DISTRIBUTION FOR VIVA VOCE : 20 MARKS

Instead of Project Work; Core Paper –

IX “BIOMEDICAL INSTRUMENTS” may be offered in the VIth Semester.

8. PASSING MINIMUM:

A candidate shall be declared to have passed the examination only if the candidate secures a minimum of 40% in the University examination and with an overall total of 40 out of 100.

9. RESTRICTIONS TO APPEAR FOR THE EXAMINATIONS

Candidates who fail in any of the course of Part I , II , III , IV & Part V of UG Degree examinations shall complete the course concerned within 5 years from the date of admission to the said programme and should they fail to do so, they shall take the examination in the revised syllabus prescribed for the immediate next batch of candidates.

If there is no change in the syllabus they shall appear for the examination in that course with the syllabus in vogue until there is a change in the syllabus. In the event of removal of that course consequent to change of regulation and/or curriculum after 5-year period, the candidates shall have to take up an equivalent course in the revised syllabus as suggested by the Chairman and fulfill the requirements as per the regulation curriculum for the award of the degree.

10. IMPROVEMENT OF MARKS IN THE SUBJECTS ALREADY PASSED

Candidates desirous of improving the marks awarded in a passed subject in their first attempt shall reappear once within a period of subsequent two semesters. The improved marks shall be considered for classification but not for ranking. When there is no improvement, there shall not be any change in the original marks already awarded.

11. CLASSIFICATION OF SUCCESSFUL CANDIDATES

A candidate who passes all the examinations in Part I to Part V securing following CGPA and Grades shall be dec

laredasfollowsforPartIorPartIIorPartIII.

| CGPA | GRADE | CLASSIFICATION OF FINAL RESULT |
|-------------------------|-------|--------------------------------|
| 9.5 -10.0 | O+ | FirstClass-Exemplary |
| 9.0and abovebutbelow9.5 | O | |
| 8.5and abovebutbelow9.0 | D++ | FirstClasswithDistinction |
| 8.0and abovebutbelow8.5 | D+ | |
| 7.5and abovebutbelow8.0 | D | |
| 7.0and abovebutbelow7.5 | A++ | FirstClass |
| 6.5and abovebutbelow7.0 | A+ | |
| 6.0and abovebutbelow6.5 | A | |
| 5.5and abovebutbelow6.0 | B+ | SecondClass |
| 5.0and abovebutbelow5.5 | B | |
| 4.5and abovebutbelow5.0 | C+ | ThirdClass |
| 4.0and abovebutbelow4.5 | C | |

12. RANKING

A candidate who qualifies for the UG degree course passing all the examinations in the first attempt, within the minimum period prescribed for the course of study from the date of admission to the course and secures I or II class shall be eligible for ranking and such ranking shall be confined to

10% of the total number of candidates qualified in that particular branch of study; subject to a maximum of 10 Ranks. The improved marks shall not be taken into consideration for ranking.

13. COMMENCEMENT OF THIS REGULATION:

These regulations shall take effect from the academic year 2021-2022, i.e., for students who are to be admitted to the first year of the course during the academic year 2021-2022 and thereafter.

14. COURSE EQUIVALENCE:

The Three Year Course in the Bachelor of Science in Electronics & Communication is Equivalent to:

- B.Sc. Electronics
- B.Sc. Industrial Electronics
- B.E.S. (Bachelor of Electronic Science)

15. COURSE OBJECTIVES:

The syllabus of B.Sc., Electronics and Communication is enriched and necessary changes have been made in the course pattern and papers. This will enable the students to acquire the **Basic & Fundamental Knowledge In Both Theory And Practical's.**

16. ACADEMIC OPPORTUNITIES:

After successful completion of this course ; students can pursue higher degree courses like M.Sc., (Electronics) / M.Sc., (Electronic Science) / M.Sc., (Applied Electronics) / M.Sc., (Electronics & Instrumentation) / M.Sc., (Cyber Forensics & Information Security) / M.Sc., (Criminology & Criminal Justice Science) / M.Sc., (Computer Science) / MCA / M.Sc., (Nano Science and Technology) / M.Sc., HRD Psychology / M.Sc., (Energy Science) / M.Sc., Environmental Science) / M.Sc., Library & Information Science) / M.Sc., YOGA / MBA / MA., (Defense & Strategic Studies / MA., Yoga / MA., Public Administration / MA., Political Science etc

17. JOB OPPORTUNITIES:

The Students are eligible for placement in Army, Navy, Airforce, Civil Aviation Sectors, Space & Radio Astronomy Sectors, T.V. Broad Casting Stations, All India Radio, BSNL , TNEB , ITES (IT Enabled Software Services) , Telecommunication Sectors, Banking and Railway Services and as Computer Hardware & Instrument Service Professionals. A Wide variety of Self – Employment Opportunities are also available.

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER I
CORE I- SEMICONDUCTOR DEVICES**

| L | T | P | C |
|---|---|---|---|
| 4 | 0 | 0 | 4 |

COURSE OBJECTIVES:

This course provides the important aspect of semiconductors and devices like diodes, Transistor, JFET and MOSFET.

| Course Outcomes: After completion of this course, student able to | |
|--|---|
| CO1 | Understand the band diagram, type of semiconductors and charge carrier life time. |
| CO2 | Understand of capacitors and types of diodes breakdown |
| CO3 | Demonstrate the transistor and their types |
| CO4 | Understand the fundamentals for construction of different type of JFET. |
| CO5 | To study the operating principles of MOSFET. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programmes specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|--------------------------|--|-----------------------------|
| I | Atom | Structure of Atom - Atomic Number - Valence Electrons - Bonding in Conductors - Insulators - Semiconductors - Energy Band Diagram of Conductors - Insulators - Semiconductors - Intrinsic Semiconductor - Extrinsic Semiconductor - P Type Semiconductor - N type Semiconductor - Carrier Life Time. | 10 |
| II | PN Junction Diode | Theory of PN Junction Diode - Energy Band Structure - Diode Current Equation - Diode Resistance - Depletion Capacitance - Diffusion Capacitance - Effect of Temperature - PN Junction Diode as a Rectifier - Zener Diode - | 10 |

| | | | |
|------------|-------------------|--|----|
| | | AvalancheBreakDown-ZenerBreakDown-ZenerDiode asa VoltageRegulator. | |
| III | Transistor | OperationofPNP&NPNTransistor-CB,CE,CC Configuration and Characteristics - Transistoras anAmplifier. | 10 |
| IV | JFET | Construction-Operation-Output&TransferCharacteristics of P Channel & N Channel JFET -Characteristic Parameters of the JFET - BiasingtheFET-ComparisonofJFET&BJT-Comparison of P Channel & N Channel JFET - ApplicationsofJFET-JFETasaVoltageVariableResistor. | 10 |
| V | MOSFET | Construction,Operation,Output&TransferCharact eristicsofPChannel&NChannelDepletionMOSFET-Construction,Operation,Output & Transfer Characteristics P Channel & NChannelEnhancementMOSFET-BiasingtheMOSFET - Comparison of P Channel MOSFETwith N Channel MOSFET - Comparison of JFETwithMOSFET-HandlingPrecautionsfortheMOSFET. | 10 |

REFERENCEBOOKS:

1. ElectronicDevices&Circuits-Salivahanan-TMH-2ndEdition
2. ATextBookofAppliedElectronics-R.S.Sedha-S.Chand-Rs.395/-

**B.SC. ELECTRONICS AND
COMMUNICATIONSEMESTR-I**

SKILLED BASED ELECTIVE

COURSES BECI-

APPLIEDELECTRICCIRCUITS

(INDEPTHTHEORY&ANALYSIS NOTREQUIRED)

| | | | |
|----------|----------|----------|----------|
| L | T | P | C |
| 2 | 0 | 0 | 2 |

COURSEOBJECTIVES:

- To understand and acquire knowledge about various circuit theorem.
- To make students strong in electric circuit designing.
- To learn the different laws and implement in circuits and also compare its gain.

COURSEOUTCOMES: After completion of this course, student able to

| | |
|------------|---|
| CO1 | Examine the basic components in series and parallel connection. |
| CO2 | Have a thorough understanding of the different circuit laws. |
| CO3 | Understand various circuit theorems. |
| CO4 | Analyse RMS & Power parameters of AC circuits. |
| CO5 | Acquire the knowledge of RLC and its applications. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|-------------------|--|-----------------------------|
|-------------|-------------------|--|-----------------------------|

| | | | |
|---|---------------------------|---|----|
| I | Circuit components | Resistors, Capacitors & Inductors in Series and Parallel - Factors governing the Resistance of a Resistor, Capacitor & Inductor - Colour Coding of Resistors - Energy Stored in a Capacitor - Energy Stored in an Inductor - Various Other Passive & Active Devices - | 05 |
|---|---------------------------|---|----|

| | | | |
|-----|---|--|----|
| | | Simple Problems. | |
| II | Circuit laws | Ohms Law - Kirchoff's Voltage Law - Kirchoff's Current Law - Current Division - Voltage Division - Star Connection - Delta Connection - Series Circuits - Parallel Circuits - Series & Parallel Circuits - Open Circuit - Short Circuit - Simple Problems. | 05 |
| III | Theorems | Super Position Theorem - Thevenin's Theorem - Norton's Theorem - Millman's Theorem - Maximum Power Transfer Theorem - Simple Problems | 05 |
| IV | Ac circuit basics | Sinusoidal and Non Sinusoidal Waveforms - Peak Value - Peak to Peak Value - Average Value - RMS Value - Period and Frequency Measurement - Power Factor - Real Power - Reactive Power - Simple Problems. | 05 |
| V | Reactance, impedance & resonance | Capacitive Reactance - Inductive Reactance - Impedance - RL and RC in Series and Parallel - RLC in Series and Parallel - Series Resonance - Parallel Resonance - Simple Problems | 05 |

Books for Reference

1. Circuits And Networks: Analysis And Synthesis - Sudhakar & Shyam Mohan - TMH - IV Edition
2. Basic Electronics - Bernard Grob - Mcgraw Hill.
3. Circuit Theory - Salivahanan - S. Chand - Rs. 395/-

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER II**

| | | | |
|----------|----------|----------|----------|
| L | T | P | C |
| 4 | 0 | 0 | 4 |

CORE II-APPLIED DIGITAL ELECTRONICS

COURSE OBJECTIVES:

- To acquire the basic knowledge of digital logic levels and application of knowledge to understand digital electronic circuits.
- To prepare students to perform the analysis and design of various digital electronic circuits.
- To learn the design process of registers, counters and conversion of analog to digital conversion and vice-versa.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|--|
| CO1 | Examine the structure of various number systems and its application in digital design. |
| CO2 | Have a thorough understanding of the fundamental concepts and minimization techniques used in digital electronics. |
| CO3 | Understand various combinational logic circuits and its applications |
| CO4 | Analyse and design various sequential logic circuits and its applications. |
| CO5 | Analyse the logic levels and apply them for the design of analog to digital conversion and vice-versa. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|-------------------|--|-----------------------------|
|-------------|-------------------|--|-----------------------------|

| | | | |
|------------|-------------------------------|--|----|
| I | Number systems | Binary Signals–Binary Number System–Decimal Number System - Octal Number System – Hexadecimal Number System– Conversion from One Number System to Another Number System–BCD–Graycode –Excess –3 Code–ASCII code. | 10 |
| II | Boolean algebra | Binary Addition, Subtraction, Multiplication & Division - 1's and 2's Complement Subtraction - 9's & 10's Complement Subtraction - Basic laws of Boolean Algebra - Duality Theorem – De Morgan's Theorem - Sum of Products–Product of Sum-Two Variable, Three Variable & Four Variable Karnaugh Maps. | 10 |
| III | Combinational Elements | Logic Gates: AND, OR, NOT, EX-OR, EX-NOR, NAND & NOR-Logic Gates using Discrete Components - NAND & NOR as Universal Gates - Half & Full Adder – Half & Full Subtractor - Encoder - Decoder- Multiplexer- Demultiplexer- Implementation using 74147, 7442, 74153 & 74155 IC's. | 10 |
| IV | Sequential Elements | Flip Flops: RS - Clocked RS - JK - Master Slave JK - D & T Flip Flops–Shift Registers: SIPO–SISO–PIPO – PISO – Shift Left – Shift Right- Ring counter – Twisted Ring Counter . Counters: Hexadecimal Up - Hexadecimal Down - Modulo Up - Modulo Down - UP/DOWN Counters-Implementation Using 7476, 7495, 7493 & 7490 IC's. | 10 |

| | | | |
|---|--------------------------|---|----|
| V | A/DAND D/A Conversion | ParallelComparatorTypeofADC- CounterRampTypeofADC- SuccessiveApproximationTypeofADC - Dual Slope Type of ADC - ADC Accuracy andResolution - Binary weighted Resistor type of DAC -R- 2RLadderTypeofDAC-DACAccuracyandResolution- ImplementationusingADC0809&DAC 0800 IC's. | 10 |
|---|--------------------------|---|----|

Booksforstudy

1. *AnandKumar*.2008.**FundamentalsofDigitalCircuits**.[SecondEdition].PHI.
2. *Salivahanan*.2004.**DigitalCircuitsandDesign**.[FourthEdition].S.Chand.

Books forReference

1. *Donald,P.Leach, Albert Paul Malvino and GoutamSaha*. 2008. **Digital Principles andApplications**.[SixthEdition].Tata McGrawHill,NewDelhi.
2. *Virendra Kumar*.2009. **Digital Technology Principles and Practice**.[First Edition].NewAge InternationalPublications,NewDelhi.
3. *Jacob Millman and Christos Halkias*. 2011. **Integrated Electronics Analog and DigitalCircuitsandSystems**.[SecondEdition].TataMcGrawHillPublishingCompanyLimite d,NewDelhi.
4. *Thomas, L.Floyd*. 2006. **Digital Fundamentals**. [Ninth Edition]. Pearson Education, NewDelhi.

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER**

RII

| | | | |
|----------|----------|----------|----------|
| L | T | P | C |
| 2 | 0 | 0 | 2 |

SKILLED BASED ELECTIVE

COURSES BEC II-

POWER ELECTRONICS

(IN DEPTH THEORY & ANALYSIS NOT REQUIRED)

COURSE OBJECTIVES:

- To understand and acquire knowledge about various power semiconductor devices.
- To provide the students a deep insight into the working of different switching devices with respect to their characteristics.
- To study the principle of operation, design and synthesis of different types of power supplies and their applications.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|---|
| CO1 | Ability to express characteristics of SCR, TRIAC, DIAC and UJT |
| CO2 | Acquire knowledge about fundamental concepts and methods to turn ON and turn OFF the thyristor. |
| CO3 | Understand the design principle of triggering circuit of SCR |
| CO4 | Explain the switching principle and applications |
| CO5 | Remember the working principle of various types of power supplies. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|-------------------|--|-----------------------------|
|-------------|-------------------|--|-----------------------------|

| | | | |
|----------|----------------------------------|---|----|
| I | Thyristor Characteristics | Simple Theory & Characteristics of SCR-DIAC, TRIAC-UJT. UJT as an Oscillator. | 05 |
|----------|----------------------------------|---|----|

| | | | |
|------------|--|--|----|
| II | Methods of Turn ON & Turn OFF | AC gate Triggering- R Triggering- RC Triggering-DC gate Triggering - Pulse gate Triggering – Natural Commutation - Force Commutation – Self - Impulse -Resonant-Complementary-External-Loadside-Line Side. | 05 |
| III | Triggering SCR | Triggering of series connected SCR's-Triggering of Parallel Connected SCR's – Current & Voltage Protection-Snubber Circuit. | 05 |
| IV | Static Switches | Single Phase AC Switches- Three Phase AC Switches- Three Phase Reversing Switches- AC Switches for Bus Transfer -DC Switches -Solid State Relays. | 05 |
| V | Power Supplies | Switched mode DC Power Supplies- Resonant DC Power Supplies- Bidirectional Power Supplies- Switched mode AC Power Supplies-Resonant AC Power Supplies-Bidirectional AC Power Supplies. | 05 |

Books for study

1. Power Electronics-Muhammed H. Rashid-PHI-2nd Edition
2. Power Electronics–Jaganathan–PHI–II Edition.

Books for Reference

1. Singh MD and Khan Chandani KB, 2007, **Power electronics** [Second Edition], Tata Mcgrawhill, New Delhi.
2. Mithal. G. K, 2000, **Industrial electronics and control** [Eighteenth Edition], Tata Mcgrawhill, New Delhi
3. Theraja B.L, Theraja. A.K, 2003, **Electrical Technology** [First Edition], S. Chand, New Delhi.

B.SC. ELECTRONICS AND**COMMUNICATION SEMESTER I****& II**

| L | T | P | C |
|---|---|---|---|
| 2 | 2 | 2 | 4 |

CORE PRACTICAL I - BASIC ELECTRONICS LAB**(ANY HARDWARE BASED SIMULATION TOOL MAY ALSO BE USED)****(Any 22 Experiments)****COURSE OBJECTIVES:**

Basic electronics laboratory is the gateway of the electronics & communication engineering world. At the very beginning the students get familiarized with the various electronics instruments & components which basically equip them to construct complex circuits in near future. In this lab students build up preliminary electronic circuits and verify the results with theoretical concepts. Lab experiments are designed in such a fashion that the engineering skill of the students starts to enrich.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|---|
| CO1 | Ability to express characteristics of DIODES, BJT, THYRISTORS |
| CO2 | To understand the basic theorems and to verify their operation |
| CO3 | To understand the basic digital circuits and to verify their operation |
| CO4 | Learn the basics of gates & Construct basic combinational circuits and verify their functionalities |
| CO5 | Learn about Shift registers & counters |

Syllabus

1. Colour Coding of Resistors
2. PN Junction Diode Characteristics.
3. Zener Diode Characteristics.
4. CE Input Characteristics.

5. CEOutputCharacteristics.
6. SCR/TRIACCharacteristics.

7. DC Regulated Power Supply Using Zener Diode.
8. Verification of Ohm's Law.
9. Verification of Kirchoff's Current Law.
10. Verification of Kirchoff's Voltage Law.
11. Verification of Thevenin's Theorem/Norton's Theorem.
12. Verification of Millman's Theorem
13. Verification of Maximum Power Transfer Theorem
14. Inductance Calculation Using Series or Parallel Resonance.
15. Transient Response of an RC Circuit.
16. Truth Table Verification of Basic Gates (Any Two)
17. Logic Gates Using Discrete Components (Any One).
18. +5V Regulated Power Supply.
19. NAND or NOR as a Universal Gate (Any One Gate).
20. Verification of De Morgan's Theorem.
21. Truth Table Verification of Half Adder & Full Adder
22. Truth Table Verification of Half Subtractor & Full Subtractor.
23. Encoder Using 74147 IC
24. Decoder Using 7442 IC
25. Multiplexer Using 74153 IC
26. Demultiplexer Using 74155 IC
27. MSJK Flip Flop Using 7476 IC
28. Parallel In Parallel Out Shift Register Using 7495 IC
29. Up Counter Using 7490 IC or 7493 IC.
30. Clock Generation Using NAND or NOR Gate.

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER III
CORE III-ELECTRONIC CIRCUITS**

| L | T | P | C |
|---|---|---|---|
| 4 | 0 | 0 | 4 |

COURSE OBJECTIVES:

To analyze and describe the applied electronics principles used to develop circuits and systems & To understand the fundamentals of the alternating current. To apply the basic tools and test equipment used to construct, troubleshoot, and design standard electronic circuits.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|---|
| C01 | Understand the basics of electrical energy and practical implementation of electrical fundamentals. |
| C02 | solve design problems on rectifiers, filters and power supply circuits. |
| C03 | Understand various types of amplifier. |
| C04 | Examine the basic components of feedback & its types. |
| C05 | Acquire the knowledge about oscillators, Multivibrators and Wave shaping circuits. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|----------|-----------------------|---|----------------------|
| I | Power supplies | Half Wave Rectifier - Full Wave Rectifier - Bridge Rectifier - Average value - RMS value - Form factor - Peak factor - Ripple factor - Efficiency - TUF - PIV - Filters: C, L, LC, CLC, CRC - Voltage Regulators : Series Regulators - Shunt Regulators - IC Voltage | 10 |

| | | | |
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| | | Regulators(78XX&79XX).. | |
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|------------|----------------------------|---|----|
| II | Transistor biasing | Bias Stability- Thermal runaway- Methods of transistor Biasing- Bias compensation- Wave Shaping Circuits – RC & RL Circuits – Clipping & Clamping Circuits– Voltage Doubler– Tripler– Quadrupler. | 10 |
| III | Types of amplifiers | Class A Amplifier - Class B Amplifier - Class AB Amplifier - Push Pull Amplifier - Complementary symmetry Push Pull Amplifier - Class C Amplifier – Multistage Amplifiers: RCC Coupled Amplifier- Transformer Coupled Amplifier- Direct Coupled Amplifier. | 10 |
| IV | Feedback | Basic concepts of Feedback- Effects of negative feedback on gain, Bandwidth, Distortion, Noise, Input Impedance and Output Impedance - Types of Negative Feedback- Voltage Series- Voltage Shunt- Current Series and Current Shunt Feedback | 10 |
| V | Oscillators | Classification of Oscillators - Barkhausen Criterion - Hartley Oscillator- Colpitt Oscillator- Clapp Oscillator - Phase Shift Oscillator- Wein Bridge- Crystal Oscillator- Frequency stability of Oscillators- Astable Multivibrator- Monostable Multivibrator Bistable Multivibrator- Schmitt Trigger. | 10 |

REFERENCE BOOKS:

1. Electronic Devices & Circuits- S. Salivahanan- TMH- II Edition
2. A Text Book of Electronic Devices & Circuits – R. S. Sedha – S. Chand Rs. 325/-

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER
R III**

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| L | T | P | C |
| 2 | 0 | 0 | 2 |

NONMAJOR ELECTIVE COURSE

IGROUP A - PAPER I -BASIC ELECTRONICS -

I(INDEPTH THEORY & ANALYSIS NOT REQUIRED)

COURSE OBJECTIVES:

This course provides the important aspect of semiconductors and devices like diodes, Transistor, JFET and MOSFET & to understand and acquire knowledge about various circuit theorem, make students strong in electric circuit designing.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|--|
| CO1 | Understand the fundamentals for construction & different type of Diode, BJT. |
| CO2 | Understand the fundamentals for construction of different type of JFET, MOS, etc |
| CO3 | Understand various circuit Laws. |
| CO4 | Examine the basic components in series and parallel connection. |
| CO5 | Analyze RMS & Power parameters of AC circuits. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|-------------------|--|-----------------------------|
|-------------|-------------------|--|-----------------------------|

| | | | |
|----------|-----------------------------|---|----|
| I | Semiconductor theory | Intrinsic Semiconductor – Extrinsic Semiconductor - Theory of PN Junction Diode-Zener Diode-Avalanche Breakdown-Zener Break Down-Operation of PNP & NPN Transistor - CB, CE, CC Configuration and Characteristics-Transistor as an Amplifier. | 05 |
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| | | | |
| II | Electronic components | Simple Theory & Use of: Resistors, Capacitors, Inductors, Diodes, Zener diodes, Transistors, FET, MOSFET, UJT, SCR, DIAC, TRIAC, LED, Seven Segment Display, Basic Gate IC's, Transformers, LDR, Switches, Microphone, Loudspeaker, Buzzers, Fuse. | 05 |
| III | Circuit laws | Ohm's Law - Kirchoff's Current Law - Kirchoff's Voltage Law - Voltage Division - Current Division - Series Circuits - Parallel Circuits - Series & Parallel Circuits - Open Circuit - Short Circuit. | 05 |
| IV | Resistors, capacitors & inductors | Resistor Resistors, Capacitors & Inductors in Series and Parallel - Factors governing the Resistance of a Capacitor & Inductor - Colour Coding of Resistors - Energy Stored in a Capacitor - Energy Stored in an Inductor | 05 |
| V | Waveforms | Sinusoidal Waveform - Non-Sinusoidal Waveforms - Peak Value - Peak to Peak Value - Average Value - RMS Value - Period & Frequency Measurement. | 05 |

REFERENCE BOOKS:

1. Electronic Devices & Circuits - Salivahanan - TMH - 2nd Edition
2. Principles of Electronics - V.K. Mehta - S. Chand.
3. Circuits & Networks - Sudhakar - TMH - 4th Edition.

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER III
NON MAJOR ELECTIVE COURSE I**

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**GROUP A- PAPER II- BIOMEDICAL ELECTRONICS – I
(IN DEPTH THEORY & ANALYSIS NOT REQUIRED)**

COURSE OBJECTIVES:

- To illustrate origin of biopotentials and its propagations
- To understand the different types of electrodes and its placement for various recordings
- To understand various physiological recordings and its measurements
- To learn the medical imaging equipment and techniques for surgeries.
- To summarize therapeutic equipment and Patient monitoring techniques for biological operations.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|--|
| CO1 | Differentiated different biopotentials and its propagations. |
| CO2 | Illustrated different electrodes and its placement for biopotentials recordings and measurements |
| CO3 | Demonstrate various electrophysiological recordings and measurements |
| CO4 | Explain medical imaging equipment's using in surgeries |
| CO5 | Demonstrated different therapeutic equipment's and Patient monitoring techniques |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|-------------------|--|-----------------------------|
|-------------|-------------------|--|-----------------------------|

| | | | |
|------------|----------------------------------|---|----|
| I | Introduction | Introduction to Human Physiology – Micro Electrodes – Skin Surface Electrodes – Needle Electrodes – Reference Electrodes. | 05 |
| II | Meters & recorders | Digital Thermometer – Sphygmo Manometer - Electronic Stethoscope – ECG – EEG – EMG | 05 |
| III | Cardio Tocography | Cardio Tocography - Electro Oculography - Electro Retinography - Poly Somnography Blood Flow Meter - Doppler – Audiometer | 05 |
| IV | Operation heat equipments | Upper Endoscope - Lower Endoscope - ENT Endoscope | 05 |
| V | Diathermy | Diathermy - Surgical Diathermy - Micro Wave Diathermy - Multipara Patient Monitor. | 05 |

REFERENCE BOOKS:

1. Biomedical Instrumentation & Measurements – Ananda Natarajan – PHI – Rs. 275/-
2. Biomedical Instrumentation and Measurements – Leslie Cromwell – PHI – 2nd Edition.
3. Bio-Medical Instrumentation – Dr. M. Arumugam – Anuradha Agencies – 2nd Edition
4. Handbook of Biomedical Instrumentation – R. S. Khandpur – TMH
5. Medical Instrumentation, Application and Design – John G. Webster – WEL – 3rd Edition
6. A Text of Book of Medical Instruments – Ananthi – New Age International – Rs. 275/-
7. Internet : Additional Reference For All Units.

B.SC.ELECTRONICSANDCOMMUNICATION**SEMESTERIII****NON MAJOR ELECTIVE COURSE****IGROUPA-PAPERIII -CELLULARPHONES****(INDEPTHTHEORY &ANALYSIS NOTREQUIRED)**

| | | | |
|----------|----------|----------|----------|
| L | T | P | C |
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COURSEOBJECTIVES:

TogetknowledgeaboutCellularRadio,ElementsofaCellularNetwork,CellularTelephony, Radio Propagation andits applications&themobileservicingandSoftwareRepairing.

COURSEOUTCOMES:Aftercompletionofthiscourse,studentableto

| | |
|------------|---|
| CO1 | UnderstandthefundamentalconceptsofCellular. |
| CO2 | Understandthedifferentaccessingtechnology. |
| CO3 | Understandthehardware&softwareofmobile |
| CO4 | Understandtheservicingofmobile |
| CO5 | Understandtheothermobileservicetools |

Syllabus

| Unit | UnitTitle | IntendedlearningChapters(ProgrammespecificqualificationattributesK1,K2, K3,K4) | Hours ofInstruction |
|-------------|------------------|--|----------------------------|
| I | Basics | WorkingofaTelephone-LocalExchange-Initiatinga call - Calling a Number - Making a Connection - Answeringa Call-Conversation- Ending a Call - HookSwitch-Transmitter-Receiver-Ringer-Cellular Mobile Telephone System - Mobile PhoneServiceArea-MobileFraudCall. | 05 |

| | | | |
|------------|------------------------------------|--|----|
| II | Access technologies | GSM-CDMA-GPRS-EDGE-WCDMA-UMTS-HSDPA-SatellitePhones-GPS-MobileBrowsers-WAP. | 05 |
| III | Hardware/Software | Types of: Wireless Options, Batteries, Memory Cards, Messaging, Ring Tones, Keypad Types, Display Types. Handset Form Factor-SMS Abbreviations-Mobile OS. | 05 |
| IV | Hardware/software repairing | Hardware/Software Repairing-Variou Locks-Installation of : UFS Driver, UFS Suite & Flashing Files-IMEI Number Detection-Mobile GSM Utility Codes (Any Five of Nokia Set) | 05 |
| V | Other mobile service tools | Ultrasonic Cleaner-Computer Connectors-SIM Card Reader - Memory Card Reader - Mobile Virus - Virus Prevention - Removing Virus - Health Hazards with Mobiles-SAR. | 05 |

REFERENCE BOOKS:

1. Modern Mobile Phone Introduction & Servicing - Manahar Lotia - BPB - Rs. 75/- (Unit-I)
2. Modern Mobile Phone Repair Using Computer Software & Service Devices - Manahar Lotia - BPB - 120/- (Units I, IV & V)
3. Modern Mobile Phone Unlocking & Utility Codes For GSM & CDMA Phones - Manahar Lotia - BPB - Rs. 99/- (Unit-IV).
4. Mobile Telephony - Digit Magazine - Supplement - Jan 2006 - Jasubhai Digital Media Publications. (Unit II & III)
5. Blue Tooth Technology - CSR Prabhu & A Prathap Reddi - PHI - Rs. 250/-

6. Mobile&PersonalCommunicationSystems&Services-RajPandya-PHI-Rs.250/-
7. INTERNET:ADDITIONALREFERENCEFORALLUNITS.

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER IV
CORE IV - 8085 MICROPROCESSOR & INTERFACING**

| | | | |
|----------|----------|----------|----------|
| L | T | P | C |
| 4 | 0 | 0 | 4 |

COURSE OBJECTIVES:

To understand the features and applications of 8085 microprocessor & exploit the abilities for the design and peripheral interfacing with real time domains

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|---|
| CO1 | Understand the evolution of processor and 8085 architectures. |
| CO2 | Learn the instruction and to create the ALP. |
| CO3 | Investigate the instruction format and machine cycle fetch. |
| CO4 | Compute and design for time delay and counters. |
| CO5 | Design and development of interfacing and applications. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|----------------------------|---|-----------------------------|
| I | 8085 Microprocessor | Signal on 8085 – Architecture of 8085 – Demultiplexing the Bus – Generating Control Signals – Fetching, Decoding and Execution of an Instruction – Memory Mapping for a 8K Memory Chip – Study of EPROM (2764) & Study of RWM (6264). | 10 |

| | | | |
|------------|------------------------|---|----|
| II | Instruction set | Data Transfer – Arithmetic – Logical – Branching - MachineControlInstructions- Stack&StackOperations-SimplePrograms. | 10 |
| III | Timing Diagram | Addressing Modes - Instruction Format - MemoryRead Machine Cycle (MOV C,A& MVI A,32) -MemoryWriteMachineCycle(MVIM,48)- TimingDiagram of IN & OUT Instruction - Interrupt Systemof8085. | 10 |
| IV | Peripherals | TimeDelayProgram–Signalson8255–I/OMode –BSRMode- InterfacingWith:Switches&MatrixKeyboard. | 10 |
| V | Interfacing | Interfacing With: LED'S - Single & Multiple SevenSegment Displays - LCD - ADC 0809 - DAC 0800 –StepperMotor -TrafficLightControl System. | 10 |

REFERENCEBOOKS:

1. MicroprocessorArchitecture,ProgrammingandApplicationsWiththe8085/8080A– Ramesh.SGaonkar-NewAgeInternational–5thEdition.
2. IntroductiontoMicroprocessor-APMathur-TMH–3rdEdition
3. MicroprocessoranditsApplications –S.Malarvizhi–AnuradhaPublications
4. FundamentalsofMicroprocessorsandMicroControllers–B.Ram–DhanpatRai– IVthEdition

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER III & IV
CORE PRACTICAL II - ELECTRONIC CIRCUITS LAB**

| L | T | P | C |
|---|---|---|---|
| 2 | 2 | 2 | 4 |

(ANY HARDWARE BASED SIMULATION TOOL MAY ALSO BE USED)

(Any 22 Experiments)

Course Objectives

The objective of the course is to equip the students with in-depth basic concepts and understanding of the principles of operation, construction, and characteristics of basic electronic equipment, and their utilization in basic electronics building blocks (or modules) and their performances practically. The techniques of analysis and design of basic building blocks of modern technology using device would be emphasized.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|---|
| CO1 | To develop hands-on skills and knowledge about the electronic devices such as oscilloscopes, function generators, multimeter, etc. |
| CO2 | To implement different types of electronic circuits using the techniques, skills. |
| CO3 | To analyze complex networks of resistors, inductors, capacitor subject to both direct (non-time-varying) and alternating voltages and currents. |

Syllabus

1. Amplitude and Frequency Measurement Using CRO
2. Half Wave Rectifier with Capacitor Filter
3. Full Wave Rectifier with Capacitor Filter
4. Bridge Rectifier with Capacitor Filter
5. Regulated Dual Power Supply Using 78XX & 79XX
6. Positive & Negative Clipping at 0V
7. Positive & Negative Clamping at 0V
8. Voltage Doubler / Voltage Tripler / Voltage Quadrupler

9. Basic Differentiator & Basic Integrator
10. Hartley Oscillator Using Transistor
11. Colpitt's Oscillator or Clapp Oscillator Using Transistor.
12. Phase Shift/Wein Bridge Oscillator Using Transistor.
13. Crystal Oscillator Using Transistors.
14. UJT Astable Oscillator.
15. Astable Multivibrator Using Transistors
16. Monostable Multivibrator Using Transistors.
17. Bistable Multivibrator Using Transistors
18. Schmitt Trigger Using Transistors.
19. Single Stage RC Coupled Amplifier.
20. Two Stage RC Coupled Amplifier
21. Emitter Follower.
22. Push Pull Complementary Symmetry Emitter Follower.
23. Power Amplifier Using LM380 IC.
24. Firing Angle Control Using SCR (Half Cycle/Full Cycle).
25. Lamp Dimmer
26. Automatic Street Light Control.
27. Transistor Chopper.
28. Burglar Alarm or Fire Alarm
29. Power Transistor Inverter (40W/20W)
30. Commutation Techniques (Any Two)

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER III&IV
CORE PRACTICAL III**

| L | T | P | C |
|---|---|---|---|
| 2 | 2 | 2 | 4 |

(ANY EMBEDDED BASED SIMULATION TOOL MAY ALSO BE USED)

(Any 22 Experiments)

8085 MICROPROCESSOR & INTERFACING LAB

Course Objectives

To expose students to the operation of typical microprocessor (8085) simulation tool/ trainer kit. solve different problems by developing different programs & to develop the quality of assessing and analyzing the obtained data.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|--|
| CO1 | Identify relevant information to supplement to the Microprocessor. |
| CO2 | Setup programming strategies and select proper mnemonics and run their program on the trainer kit boards/simulator. |
| CO3 | Practiced different types of programming keeping in mind technical issues and evaluate possible causes of discrepancy in practical experimental observations in comparison. |
| CO4 | Develop testing and experimental procedures on Microprocessor analyze their operation under different cases. |
| CO5 | Prepare professional quality textual and computational results, incorporating accepted data analysis and synthesis methods, simulation software, and word-processing tools. |

Syllabus

1. Addition of Two; 8 Bit Numbers & Subtraction of Two; 8 Bit Numbers
2. Multiplication of Two; 8 Bit Numbers
3. Division of Two; 8 Bit Numbers
4. BCD Addition

5. BCDSubtraction
6. FILL
7. BLOCKMOVE
8. 1's&2'sComplementofa16BitNumber.
9. Smallest/Largestof;NNumbers.
10. Ascending/DescendingOrderof;NNumbers.
11. SumofN;8BitNumbers.
12. MultiByteAddition.
13. 3DigitDecimaltoTwodigitHexadecimalConversion.
14. TwodigitHexadecimalto3digitDecimalConversion.
15. ADCInterface.
16. DACInterface.
17. WaveformGenerationUsingDAC.
18. StepperMotorInterface.
19. InterfacewithSwitches& InterfacewithLED's
20. HexKeyboardInterface.
21. InterfacewithSingleSevenSegmentDisplay.
22. InterfacingwithMultipleSevenSegmentDisplays.
23. InterfacewithLCD's.
24. InterfacingwithSolidStateRelay.
25. MovingDisplay.
26. BlinkingDisplay.
27. DigitalClock.
28. TrafficLightControl.

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER V**

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| L | T | P | C |
| 5 | | | 5 |

CORE V - ELECTRONIC COMMUNICATION SYSTEMS

COURSE OBJECTIVES:

Learn about the theoretical bounds on the rates of Electronic Communication System and represent a digital signal using several modulation methods. Draw signal space diagrams, compute spectra of modulated signals and apply redundancy for reliable communication.

COURSE OUTCOMES: At the end of course, student will be able to:

| | |
|------------|--|
| CO1 | Understand the basics of EM, space diagram, spectrum, propagation. |
| CO2 | Learn the generation and detection of Modulation and baseband system. |
| CO3 | Understand the generation, detection, Transmitter of FM. |
| CO4 | Learn the AM & FM Receiver |
| CO5 | Evaluate the performance of PCM, DPCM and DM in a digital communication system |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|---------------------------------------|---|---------------------------------|
| I | Propagation of radio waves | Introduction to EM waves – Reflection and refraction of radio waves at the surface of the earth – Ground wave propagation – Sky wave propagation – Space wave propagation – Structure of the Atmosphere – Critical frequency – Skip distance – Maximum Usable frequency (MUF) – Virtual height. | 10 |
| II | Am generation & tr | Need for modulation – Amplitude modulation – Frequency Spectrum of the AM Wave – Modulation Index – Power relations in the AM | 10 |

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| | | Wave – AM generation – AM Transmitter. - Forms of Amplitude Modulation – Evolution of SSB – Balanced Modulator – Methods of SSB Generation – Vestigial sideband Transmission. | |
| III | Fm generation & transmission | Frequency Modulation - Frequency Spectrum of the FM Wave – Modulation Index – Effect of Noise – Adjacent & Co- Channel Interference – Wide Band & Narrow Band FM – FM Generation – Direct and Indirect methods – FM Transmitter – Pre-Emphasis. | 10 |
| IV | Am & fm reception | AM Receiver – TRF Receiver – Super Heterodyne Receiver – Image Frequency Rejection – Frequency Changing & Tracking – Choice of IF – AM Detection – AGC – SSB Detection. FM Receiver – Amplitude Limiter – De-Emphasis – FM Detection – Balanced Slope Detector – Phase Discriminator – Ratio Detector. | 10 |
| V | Pulse modulation | PAM Modulation & Detection – PWM Modulation & Detection - PPM Modulation & Detection - Sampling Theorem – Quantization & Quantization Error – PCM Modulation & Detection – Companding – ASK – FSK – BPSK – QPSK – DPSK | 10 |

REFERENCE BOOKS:

1. Electronic Communication Systems - Kennedy - TMH - IVEd
2. Electronic Communication Systems - Roddy & Collen - PHI - IVEd
3. Electronic Communications - Sanjeev Gupta - Khanna Publications.
4. Principles of Communication Engineering - Anokh Singh - S. Chanda

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER V
CORE VI-IC'S AND THEIR APPLICATIONS**

| | | | |
|----------|----------|----------|----------|
| L | T | P | C |
| 5 | | | 5 |

COURSE OBJECTIVES:

This course elaborates the production methods of IC's and various applications of them including logic, OP-AMPs.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|--|
| CO1 | Understand the method of IC's fabrication Techniques. |
| CO2 | Study the various circuits of Logic operations using ICs. |
| CO3 | Get the knowledge of various OP-Amp circuits |
| CO4 | Study the principle of Filter. |
| CO5 | Study the principle of Timer & Phase locked loop circuits. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|-------------------------------|---|-----------------------------|
| I | IC fabrication process | Introduction - Basic Planar Process - Fabrication of a Typical Circuit - Active and Passive Components - Fabrication of FET, MOSFET & CMOS. | 10 |
| II | Logic family's | Characteristics of IC's - Diode Logic - Transistor Logic - RTL - DCTL - IIL - DTL - HTL - TTL - CMOS - ECL - Comparison of Logic Families. | 10 |

| | | | |
|------------|------------------------------------|--|----|
| III | OP – AMP 'S | Introduction - The ideal OP-AMP - OP-Amp Stages - OP-Amp Parameters - Inverting & Non Inverting Amplifier - Adder - Subtractor - Multiplier - Divider - Integrator - Differentiator - V to I Converter - I to V Converter. | 10 |
| IV | Filters | Low Pass Filter – High Pass Filter – Band Pass Filter – Band Reject Filter - Solving of Simultaneous Equations – Solving of Differential Equations. | 10 |
| V | 555 Timer & 565 PLL | 555: Functional Diagram - Astable Operation - Monostable Operation - Linear Ramp Generator. PLL: Basic Principle – 565 PLL - Frequency Translation - Frequency Multiplier - Frequency Divider - AM Detection – FM Detection. | 10 |

REFERENCE BOOKS:

- 1) Linear IC's – Roy Choudhury – NAI – 4th Edition. (UNIT I)
- 2) Electronic Circuits – Salivahanan – TMH – II Edition (UNIT II)
- 3) OP-Amps – Gayakwad – PHI – 4th Edition – (UNIT III, IV & V)

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER V
ELECTIVE I - PAPER I - Group A
8051 MICROCONTROLLER AND INTERFACING**

| | | | |
|----------|----------|----------|----------|
| L | T | P | C |
| 5 | | | 5 |

COURSE OBJECTIVES:

To understand the basics of micro controller architecture and memory systems. learn the assembly language programming instructions and writing programs & to exploit the abilities for the design and peripheral interfacing with real time domains.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|--|
| CO1 | Understand the evolution of microcontroller 8051 architectures, pin functions and bus timing. |
| CO2 | Learn the instruction format, and to create the assembly language program with looping techniques. |
| CO3 | Investigate the interfacing techniques for keyboard and optical devices. |
| CO4 | Compute and design for converter and real time applications. |
| CO5 | Design and development of memories. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|-----------------------------|--|-----------------------------|
| I | 8051 Microcontroller | Features-Signals-Architecture-RAM Structure-SFR's- Power Saving Modes-Interrupt logic-Timer Logic-Serial Logic | 10 |

| | | | |
|------------|-------------------------------------|---|----|
| II | Instruction set | Datatransfer-Arithmetic-Logical-Bitmanipulation-BranchingInstructions-Stack&StackOperations-Addressing Modes-Simple Programs. | 10 |
| III | Interfacing withIO | InterfacingWith:Switches&MatrixKeyboard-LED'S - Single & Multiple Seven Segment Displays -LCD. | 10 |
| IV | Interfacing withApplic ation | InterfacingWith:ADC0809IC-DAC0800IC-StepperMotor-DCMotor-TrafficLightControlSystem. | 10 |
| V | Memories | ROM - PROM - EPROM(2764) - EEPROM - NVRAM - Static RWM (6264)-Dynamic RWM(TC511000)-RWMRefreshing-PseudostaticRWM. | 10 |

REFERENCEBOOKS:

1. The8051MicrocontrollerEmbeddedSystems-Mazidi&Mazidi-Pearson-2ndEdition.
2. IntroductiontoMicroprocessor-AP Mathur - TMH-3rdEdition (Unit-V)

B.SC. ELECTRONICS AND

COMMUNICATION SEMESTER

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**ELECTIVE I - PAPER II-Group
APIC16F877 MICROCONTROLLER AND INTERFACING**

COURSE OBJECTIVES:

1. Through this course, the students will be exposed to hardware details of PIC16F877 MICROCONTROLLER with the related signals and their implications.
2. They will also learn programming and interfacing of PIC16F877.
3. They will also be aware of the interfacing and its application.
4. Lastly the students will have a basic idea on memory.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|--|
| CO1 | Understand the architecture of PIC16F877 MICROCONTROLLER. |
| CO2 | Impart the knowledge about the instruction set. |
| CO3 | Interface LCD, Keyboard, ADC, DAC, Sensors, Relays, DC motor and Stepper motor with PIC16F877 microcontroller. |
| CO4 | Understand the basic idea about Memories and its applications. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programmes specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|----------------------------------|--|-----------------------------|
| I | PIC16f877 Microcontroller | Features – Signals - Architecture – Memory Organization – Watch Dog Timer – Reset Types – Oscillator Types – Power Down Modes – I/O Ports – CCP Module – SSP Module. (USART, SPI, I2C & ICSP) | 10 |

| | | | |
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| II | Instructionset | Byte Oriented – Bit Oriented - Literal & Control Instructions–Stack&StackOperations-AddressingModes–SimplePrograms-Timer logic–interruptlogic–Serial Logic–ADC. | 10 |
| III | Interfacing with IO | Interfacing With: Switches & Matrix Keyboard - LED'S-Single&MultipleSevenSegmentDisplays – LCD. | 10 |
| IV | Interfacing with Application | Interfacing With: ADC 0809 IC – DAC 0800 IC - StepperMotor–DCMotor–TrafficLightControlSystem. | 10 |
| V | Memories | ROM–PROM–EPROM(2764)-EEPROM–NVRAM - Static RWM (6264)-Dynamic RWM(TC511000)- RWM Refreshing – PseudostaticRWM. | 10 |

REFERENCEBOOKS:

1. PIC16F877DataBook–MICROCHIP.
2. FundamentalsofMicrocontrollersandApplicationsInEmbeddedSystems(WiththePIC18MicrocontrollerFamily)–RameshSGaonkar–PRI–RS.300/-
3. DesignWithPicMicrocontrollers–JohnB.peatman–Pearson
4. IntroductiontoMicroprocessors–APMathur–TMH–3rdEdition(UNIT-V).

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER V
ELECTIVE I - PAPER III -Group
A PROGRAMMABLE LOGIC CONTROLLER**

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COURSE OBJECTIVES:

To provide knowledge levels needed for PLC programming and operating, PLC functions, Data Handling processes & make the students how devices to which PLC input and output modules are connected.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|---|
| CO1 | Ability to gain knowledge on Programmable Logic Controller. |
| CO2 | Create ladder diagrams from process control function |
| CO3 | Understand the knowledge about various type of PLC registers |
| CO4 | Develop the PLC application program |
| CO5 | Design the different type of PLC functions, Data Handling Function. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programmes specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|----------|-------------------|---|----------------------|
| I | PLC Basics | Advantages and Disadvantages – Overall PLC System – Input & Output Modules – Printing PLC Information – CPU – Memory – Processor – I/O Modules – Power Supplies – Programming Equipment – Programming Formats – Construction of PLC Ladder Diagram – Processors Scanning Considerations – PLC Operational Faults – Input ON/OFF Switching Devices – | 10 |

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| | | InputAnalogDevices– OutputON/OFFDevices–OutputAnalogDevices. | |
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| II | PLC Programming | Input Instructions – Outputs: Coils, Indicators & Others – Operational Procedures – Contact and Coil I/O Programming Examples (Any 3) – Digital Logic Gates – Boolean Algebra PLC Programming – Conversion Examples (Any 3) – Ladder Diagrams and Sequence Listings – Large Process Diagram Construction. | 10 |
| III | PLC Functions | General Characteristics of Registers – Module Addressing – Holding Registers – Input Registers: Single and Group – Output Registers: Single and Group – PLC Timer Functions – Examples of Timer Function Industrial Applications (Any 3) – PLC Counters – Examples of Counter Function Industrial Applications (Any 3). | 10 |
| IV | Intermediate functions | PLC Addition and Subtraction – PLC Repetitive Clock – PLC Multiplication, Division, Square Root, Trigonometric and Log Functions – Other Arithmetic Functions – Basic Comparison Functions – Basic Comparison Function Applications (Any 3). | 10 |
| V | Data handling functions | Skip Function and Applications – MASTER CONTROL RELAY Function and Applications – Jump with Non Return – Jump with Return – MOVE Function & Applications – Moving Large Blocks of PLC Data – PLC Table and Register Moves – PLC FIFO Function – FAL – ONS – CLR And SWEEP Functions – Bit Patterns in a Register – Changing a Register Bit Status – Shift Register Functions and Applications. PLC Networking. | 10 |

TEXTBOOK

Programmable LogicControllers–John W.Webb &Ronald A.Reis–PHI–V Edition – Rs.295/-

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ELECTIVE II - PAPER I - Group B**MODERN TELEVISION****SYSTEMS COURSE OBJECTIVES:**

To introduce the basics of picture transmission and reception, analysis and synthesis of composite video signal, receiver and picture tubes and television camera tubes also to study various colour television systems.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|---|
| CO1 | Understand the fundamental concepts of television standards. |
| CO2 | Acquire the knowledge in basic principle of receiver circuits. |
| CO3 | Distinguish the principle of vertical and horizontal sync separation. |
| CO4 | Understand different colour television systems used worldwide and its compatibility. |
| CO5 | Evaluate the detailed operation of camera and picture tube of monochrome and colour television. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|-------------------|--|-----------------------------|
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| I | Television Standards | Aspect Ratio - Scanning – Number of Scanning Lines - Interlaced Scanning- Vertical Resolution and Horizontal Resolution – Horizontal & Vertical Sync Details- Composite Video Signal- Channel Bandwidth – Vestigial Side Band Transmission & Reception – Complete Channel Bandwidth – FM Channel | 10 |
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| | | Bandwidth–AllocationofFrequencyBandsForTV Transmission-PositiveandNegativeModulation–CCIR– BStandards. | |
| II | Receiver Circuits | RF Tuner –Tuner Types - Various Sections of a VHFTuner – UHF Tuner – Electronic Tuning – Video IFSection – IF Amplifier – VSB Correction - Video IFamp using IC CA 3068 - Video Detector Operation &Requirements– VideoAmplifierOperation&Requirements– CouplingMethods–VideoAmpUsing IC TBA 890. | 10 |
| III | SyncSe parator | BlockDiagram–Vertical&HorizontalSyncSeparation- VerticalO/PStage&Requirements– VerticalO/PStageIC's–EHTgeneration-SCorrection– Line O/PStageUsingTransistors& ICCA 920 – AGC – Types of AGC - Sound Section –Sound Take Off Circuit – Inter Carrier Sound IF Amp –AMLimiting– FMDetection–AnyOneFMDetector -Sound Section ICCA3065. | 10 |
| IV | ColourT elevison | Compatibility–Naturallight–Colourperception– Threecolourtheory–Chromaticitydiagram–Luminance, Hue and Saturation – Luminance &Colourdifferencesignals-Frequencyinterleaving– BandwidthforColourSignalTransmission- ModulationofColourDifferenceSignals-ColourTV Standards. | 10 |

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| V | Television Transmission & Reception | Monochrome TV Camera Tubes (Any One) – Monochrome Picture Tube – Block diagram of Monochrome TV Transmitter and Receiver – Colour TV Camera – Colour Picture Tubes (Any One) – PAL-D Coder – PAL Decoder – Merits & Demerits – Low Voltage Power Supply – High Voltage Power Supply – SMPS – Merits & Demerits. | 10 |
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REFERENCE BOOKS:

1. Monochrome And Colour Television - Gulathi - Nai - Ii Edition
2. Colour Television Principles And Practice - Gulathi - Nai
3. Modern Television Practice - Gulathi - Nai - III Edition

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ELECTIVE II-PAPER II-Group B**RADAR AND NAVIGATIONAL SYSTEMS****COURSE OBJECTIVES:**

To introduce the fundamental concepts of RADAR and navigational aids.

Expose the student to different types of RADAR systems and Navigation, get familiarized with recent technologies in methods of navigation.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|---|
| CO1 | Gain knowledge in the fundamental principle of RADAR. |
| CO2 | Become familiar with fundamentals of different types of RADAR. |
| CO3 | Gain in-depth knowledge about the different types of RADAR transmitter and receiver and their operations. |
| CO4 | Understand the signal detection in RADAR and various detection techniques. |
| CO5 | Understand the navigational aids and modern navigation methods. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|------------------------------|---|-----------------------------|
| I | Introduction to Radar | Basic Radar – The simple form of the Radar Equation - Radar Block Diagram - Radar Frequencies – Applications of Radar – The Radar Equation - Transmitter Power - Pulse Repetition Frequency – Antenna Parameters - Other Radar Equation Considerations. | 10 |

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| II | MTI andPulse DopplerRad ar | DopplerandMTIRadar-MovingTargetDetector- MTIfromaMovingPlatform-PulseDopplerRadar- | 10 |
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| | | Other Doppler Radar Topics-Tracking with Radar– Automatic Tracking with Surveillance Radars. | |
| III | RADAR Transmitter and Receiver | Linear Beam Power Tubes-Solid State RFPower Sources - Magnetron - Crossed Field Amplifiers - Other RFPower Sources- Other aspects of Radar Transmitter -The Radar Receiver-Superheterodyne Receiver- Duplexers and Receiver Protectors-Radar Displays.- Propagation Radar Waves - Atmospheric Refraction – Standard propagation-Nonstandard Propagation-The Radar Antenna-Reflector Antennas. | 10 |
| IV | RADIO DIRECTION FINDING & RANGES : | An Aural Null Direction Finder – The Goniometer - Automatic Direction Finders - The Commutated Aerial Direction Finder- The LF/MF Four course Radio Range- VHF Omni Directional Range-VOR Receiving Equipment- Loran-A Equipment-The Decca Navigation System- Decca Receivers-The Omega System. | 10 |
| V | Methods of Navigation | Operation of DME-TACAN Equipment.- Instrument Landing System- Ground Controlled Approach System –Microwave Landing System-Navigation Over the Earth-Components of an Inertial Navigation System. | 10 |

REFERENCE BOOKS:

1. Introduction To Radar Systems-Skolnik–Tmh–Iii Edition 2003
2. Radar Principles-Peyton ZPeebles-John Wiley–2004
3. Principles Of Radar-Jc Toomay-Phi Ii Edition-2004

4. Microwave&RadarEngineering–Kulkarni–UmeshPublications
5. RadarSystem&RadarAidsToNavigation–Sen&Battachariya–KhannaPublications.

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER V
ELECTIVE II- PAPER III Group B
SATELLITE, CABLE AND DTH SYSTEMS**

COURSE OBJECTIVES:

- To introduce the basic concepts of Satellites and cable networks.
- To make the students assemble and troubleshoot DTH themselves.
- Create entrepreneurship opportunity.

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COURSE OUTCOMES: After completion of this course, student able to

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| CO1 | Gain knowledge about history and basics of satellites |
| CO2 | Acquire knowledge about cable TV network transmission techniques |
| CO3 | Acquire knowledge about digital satellite TV network transmission techniques |
| CO4 | Introduction of DTH Components |
| CO5 | Installing the DTH & supporting peripherals |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|--------------------------|---|-----------------------------|
| I | Satellite systems | Geo-Stationary Satellite – Satellite Communication System – Satellite Electronics – International and Regional Direct Broadcasting Satellites – Indian Domestic Satellites – Domestic Broadcasting Systems. | 10 |

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| II | Cable tv systems | CableSignalSources–CableSignalProcessing– CableSignalDistribution–BidirectionalNetworks– ScramblingofTVSignals–CableSignalConvertors. | 10 |
| III | Digital satellite tv | Digital Satellite Transmission – Digital SatelliteReceptionandDecoding–DTHTV– DigitalTV Receiver–MeritsofDigitalTVReceiver –DTT. | 10 |
| IV | Dth working | DTHAntenna–DTHLNB–DTHReceiver– AdditionalAccessories–CompleteDTHProcess– Connecting more than One TV Receiver to a SingleDish , Connecting more than one tv to a Single SatelliteReceiver – Connecting more than one Dish/LNB to aSingleReceiver– ChangingSatelliteChannels–Need ofTelephoneJack. | 10 |
| V | Dish installation | Site Survey – Dish Roof and Wall mounts – AdjustingtheAzimuthandElevationSettings– DishAntennaConnectionProcedures–Precautions– TroubleShooting–AdjustmenttoCorrectPosition– LNBTesting–DDDirectPlus–SatellitesUsed– Comparison with Other DTH Systems – Reception ofDDDirectPlus–ReceiverInstallation–TV/Radio ChannelsonDDDirectPlus. | 10 |

REFERENCEBOOKS:

1. ModernTVPractice–R.R.Gulati–NAI–IIIrdEdition
2. ModernDTHDigitalSatelliteReceiver–ManahorLotia–BPB–Rs.120/-

3. CompositeSatelliteandCableTV–R.R.Gulati–NAI.
4. SatelliteCommunication-DennisRoddy–TMH

**B.SC. ELECTRONICS AND
COMMUNICATIONSEMESTER V
ELECTIVEII-PAPERIV-GroupB
MOBILECOMMUNICATON&SERVICING**

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COURSEOBJECTIVES:

- To introduce the basics of Cellular structure and operation.
- It provides entrepreneurship opportunity & Practical oriented.

COURSEOUTCOMES: After completion of this course, student able to

| | |
|------------|---|
| CO1 | Understand the fundamental concepts of Cellular. |
| CO2 | Acquire the knowledge in basic principle of BT Stransmission. |
| CO3 | Understand the different accessing technology. |
| CO4 | Understand the chip level software servicing of mobile |
| CO5 | Understand the servicing of mobile |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|----------------------------------|--|-----------------------------|
| I | Mobile data communication | Introduction-Cellular Radio- Elements of a Cellular Network-Cellular Telephony - Radio Propagation - Speech Coding- Error Coding and Error Correction. | 10 |
| II | Mobility Management | Mobility Management - Hand Off Management- Hard Hand Off- Soft Hand Off- Switching and Authentication- MTSO Interconnections- Circuit | 10 |

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| | | SwitchedandPacketSwitchedDataServices | |
|--|--|---------------------------------------|--|

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| | | on Cellular Networks. | |
| III | Multiple access technologies | Introduction - Frequency division multiple access - Time division multiple access - Code Division Multiple Access - Spread Spectrum Techniques. | 10 |
| IV | Mobile servicing | Hardware/Software Repairing - Various Locks - Installation of: UFS Driver, UFS Suite & Flashing Files - IMEI Number Detection - Mobile Utility Codes. | 10 |
| V | Other mobile service tools | Ultrasonic Cleaner - Computer Connectors - SIM Card Reader - Memory Card Reader - Mobile Virus - Virus Prevention - Removing Virus - Health Hazards with Mobiles - SAR. | 10 |

REFERENCE BOOKS

1. Wireless Communications And Networking - Made Simple - Satish Jain - BPB Publications. Rs. 135/- (Units: I, II & III)
2. Modern Mobile Phone Repair using Computer Software & Service Devices - Manahar Lotia - BPB - 120/- (Units IV & V)
3. Modern Mobile Phone Unlocking & Utility Codes For GSM & CDMA Phones - Manahar Lotia - BPB - Rs. 99/- (Unit IV)
4. Mobile Cellular Telecommunication - II Edition - William C Y Lee - TMH
5. Mobile Communications - Schiller - Pearson - II Edition.
6. Wireless Communications - Stalling - Pearson II Edition.
7. Mobile & Personal Communication Systems & Services - Raj Pandya - PHI - Rs. 250/-

**B.SC. ELECTRONICS AND
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RV**

**SKILLED BASED ELECTIVE
COURSES BEC III - ELECTRONIC
INSTRUMENTATION (IN DEPTH THEORY
& ANALYSIS NOT REQUIRED)**

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COURSE OBJECTIVES:

- To introduce the fundamental electronic instrumentation things.
- To expose the students to upgrade their knowledge in industry side.
- To get familiarize with recent instrumentation technologies.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|---|
| CO1 | Gain knowledge in the fundamental principle of electromechanical instruments. |
| CO2 | Become familiar with Bridges Network circuits |
| CO3 | Acquire the knowledge in different types of oscilloscopes. |
| CO4 | Understand the signal generation techniques. |
| CO5 | Understand the working function and application of transducer. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|-------------------|--|-----------------------------|
|-------------|-------------------|--|-----------------------------|

| | | | |
|----------|--------------------------------------|---|----|
| I | Electromechanical instruments | DC Ammeter - DC Voltmeter - Voltmeter Sensitivity - AC Voltmeter - Considerations in Analog Voltmeter - Series & Shunt Type Ohmmeter - Calibration of DC Instruments - Study of a Typical Digital Multimeter. | 05 |
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| II | Bridges | Wheatstone Bridge - Balance Equation of General AC Bridges – Capacitance & Inductance Comparison Bridge - Maxwell – Hay - Schering - Wien - Kelvin & Kelvin's Double Bridge. | 05 |
| III | Oscilloscope | Block diagram - CRT - Vertical Deflection System - Delay line - Horizontal Deflection System - CRT screens & Graticules - Oscilloscope Probes - Measurement of Frequency, Amplitude & Phase - Lissajou's Patterns. Protocols. | 05 |
| IV | Signal generation & signal analysis | Sample & Hold Circuit - Instrumentation Amplifier - Function Generator - Pulse Generator - Q Meter - Vector Impedance Meter - Wave Analyzer - Harmonic Distortion Analyzer. | 05 |
| V | Transducers | Resistive Transducers – Inductive Transducers - Capacitive Transducers - Piezo Electric Transducer - Thermo Electric Transducers – Temperature Transducers – Microphones & Loud Speakers. | 05 |

REFERENCE BOOKS:

1. Electronic Instrumentation - H.S. Kalsi - TMH.
2. Modern Electronic Instrumentation & Measurement Techniques - Cooper - PHI.
3. Electronic Measurements & Instrumentation – Salivahanan – S.Chand -Rs.270/-

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER V
SKILLED BASED ELECTIVE
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SKILLS**

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(SIMPLE THEORY ONLY)

50 Multiple Choice Questions. EACH QUESTION 1.5 MARKS. TE

N Multiple Choice Questions From Each Unit

COURSE OBJECTIVES:

To provide students with solid foundation in CSE so that they are able to use this knowledge in getting jobs and maintaining their jobs. To develop students with professional and ethical attitude, effective communication skills and the attitude of working in group/with people for successful careers.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|--|
| CO1 | The ability to analyze a problem and to identify the appropriate Verbal reasoning. |
| CO2 | The ability to apply nonverbal reasoning. |
| CO3 | An understanding of professional, ethical and social responsibilities. |
| CO4 | The ability to communicate effectively with a range of audiences. |
| CO5 | The ability to succeed in competitive exams |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programmes specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|-------------------|---|-----------------------------|
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| I | Verbal Reasoning | Analogy–Classification–DirectionSenseTest –LogicalSequenceofWords– InsertingTheMissingCharacter–Situation ReactionTest. | 05 |
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| II | Non verbal reasoning | Analytical Reasoning – Mirror Images – Water Images-Completion of Incomplete Pattern– Cubes and Dice. | 05 |
| III | Arithmetical, ability | Average–Problems on Age– Percentage– Profit & Loss– Ratio & Proportion– Chain Rule– Time & Work. | 05 |
| IV | Logarithms | Pipes & Cistern– Time & Distance– Problems on Trains-Boats & Streams– Simple Interest– Compound Interest– Logarithms. | 05 |
| V | Charts | Area– Calendar– Clocks– Heights & Distances– Bar Graphs– Pie Charts. | 05 |

Reference Books

1. A Modern Approach To Verbal & Non Verbal Reasoning - Revised Edition – R.S. Aggarwal–S. Chand. (Units: I & II)–Rs. 750/-
2. Quantitative Aptitude- Revised Edition- R.S. Aggarwal–S. Chand. Units: III, IV & V) –Rs. 440/-
3. An Advanced Approach To Data Interpretation- R.S. Aggarwal–S. Chand.
4. Advanced Objective General Knowledge- R.S. Aggarwal–S. Chand
5. Objective General English- R.S. Aggarwal–S. Chand

B.SC. ELECTRONICS AND**COMMUNICATION SEMESTER****RVI**

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**CORE VII - PC HARDWARE NETWORKING
& TROUBLESHOOTING****COURSE OBJECTIVES:**

This course guides the complete view of hardware of the personal computer and possible troubleshooting.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|--|
| CO1 | Understand the motherboard types and connection with various types of RAM |
| CO2 | Study the various versions of BIOS and their functions. |
| CO3 | Get the knowledge of Keyboard and Mouse connections and their troubleshooting |
| CO4 | Understand the basic of Hard disk and their connections, with CD and DVD storage devices & the printer types |
| CO5 | Study the networking and their connections with assembling of PC in the cabinet. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|-----------------------|---|-----------------------------|
| I | Mother board | Components - Support Circuits – Connectors - Installation - Troubleshooting – BIOS Beep Codes - Study of a Latest Motherboard-Form Factor-PC Assembly-Cabinet Form Factor . | 10 |
| II | Memory systems | Memory Modules-Cache Memory-Shadow Memory-Common Memory Errors-Battery-BIOS Functions-BIOS Setup–BIOS Upgrade. | 10 |

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| III | Input device Troubleshooting | KEYBOARD : Organisation - Troubleshooting - Ergonomics MOUSE :Connection-Resolution-Installation-Troubleshooting.. | 10 |
| IV | Output device Troubleshooting | HARDDISK :FormFactor-StorageCapacity-DiskGeometry - Interfacing – Installation , Formatting & Troubleshooting. PRINTER :Types,Interface&Troubleshooting. | 10 |
| V | Networking | NETWORKING :SettingupaNetwork–Preparationfor Network Installation – Network Configuration – SharingComputer–SharingPrinter– TroubleshootingNetworking. VIRUS :Types-Working-Symptoms-Antivirus. | 10 |

REFERENCEBOOKS:

1. ModernComputerHardwareCourse-ManoharLotia-BPB-Rs.360/-
2. IbmPcAndClones–Govindarajulu–Tmh
3. IbmPcAdvancedTroubleShootingAndRepairingGuide–Robert.c.brenner-Phi.
4. TroubleShooting,Maintenance&RepairingPc's–stephenJ.bigelow-Tmh–IiEd
5. UpgradingandRepairingPC's-ScottMueller–Pearson-18th-Edition.
6. DOS6&6.22AnIntroduction-ManoharLotia–BPB–Rs.99/-
7. DOS6&6.22Companion-SatishJain-BPB–RS.210/-

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COREVIII-NETWORKCOMMUNICATION&SECURITY

COURSEOBJECTIVES:

- Describevariouscommunicationsnetworksandtheirmaincomponent
- Identifytheadvantagesanddisadvantagesofanetwork.
- Identifythefunctionofafirewall,andhowitkeepsacomputersecureandsafefromviruses.Prepare aplanforanti-virusprotection.

COURSEOUTCOMES:At theendofcourse,studentwillbeableto:

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| CO1 | IdentifythecomponentsassociatedwithTransmissionmethods. |
| CO2 | Studentswilldetailsofsnetworkarchitecture,Topologytechnologyetc |
| CO3 | Understandnetworksprotocolsandnetworkmanagement. |
| CO4 | Theconceptofencapsulationanditsrelationshipptolayeringinthe networkmodel. |
| CO5 | AnabilitytounderstandandanalyzetheissuesinprovidingQuality-Of-Service for network multimedia applications such as Internet, telephony&networksecurity |

Syllabus

| Unit | UnitTitle | Intended learning Chapters(Programmespecificqualification attributes K1,K2,K3,K4) | Hours ofInstruction |
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| I | Transmission methods | Digital Signal Analog Transmission – BaudRate- AnalogSignalDigitalTransmission –Parallel&SerialCommunication– Asynchronous & SynchronousCommunication – Simplex – Half Duplex -Full | 10 |

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| | | Duplex – Multiplexing - De-multiplexing-TypesofMultiplexing. | |
| II | Network | MeshTopology–StarTopology–Tree | 10 |

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|------------|---|--|-----------|
| | topologies | Topology – Ring – Bus – Hybrid – Basics of Switching – Router & Routing – Internet Topology – Architecture of an ISP – Logical Types of Topology. | |
| III | Network protocols | OSI Model – Physical Layer – Data Link Layer – Network Layer – Transport Layer – Session Layer – Presentation Layer – Application Layer – Overview of Network Protocols. | 10 |
| IV | LAN technologies | Introduction – LAN Hardware – Implementing LAN – Fast LANS – Nonstandard LANS – Extending LANS – Virtual LANS – Token Passing Networks – FDDI – MAN – WAN. | 10 |
| V | Internet access & network security | Introduction – Dialup Access – Leased lines – DSL – Cable Modems – DTE – DCE Interface – RS-232 & RS-449 Interface – SONET. NETWORK SECURITY: Introduction – Types of Computer Attacks – Firewall – Virtual Private Network – Cryptography. | 10 |

REFERENCE BOOKS:.

1. Data Communication & Networks - Achyut.S.Godbole & Atul Kahate – TMH – 2ED (Units: I, II, III & V)
2. Advanced Computer Networking (Concepts and Applications) - Satish Jain – BPB – Rs.195/- (Unit: IV & V)
3. Data Communication And Networking (UPDATED EDITION) – Satish Jain – BPB Publications. Rs.270/-
4. Computer Networks – UYLESS BLACK – PHI – IIND EDITION.
5. Computer Networks – ANDREW.S.TANENBAUM – PHI.
6. Communication Protocol Engineering - Pallapa Venkataram and S.S.Manvi - PHI.
7. Networking Concepts And Netware – Anand – Himalaya Publications

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER VI
CORE IX-BIOMEDICAL INSTRUMENTS
(Simple Theory Only) OR PROJECT WORK**

| | | | |
|----------|----------|----------|----------|
| L | T | P | C |
| 5 | 0 | 0 | 5 |

COURSE OBJECTIVES:

To introduce an fundamentals of Human Physiology and explore the human body parameter measurement setups

COURSE OUTCOMES: At the end of course, student will be able to:

| | |
|------------|---|
| CO1 | Understand the human physiology of biomedical system |
| CO2 | Measure biomedical and physiological information |
| CO3 | Discuss the application of Electronics in diagnostics and therapeutic area. |
| CO4 | Make the students understand the concepts Pulse Oximeter and pacemaker techniques |
| CO5 | Give basic ideas about modern medical imaging application |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|---|--|---------------------------------|
| I | Introduction to Human Physiology | Introduction to Human Physiology – Micro Electrodes – Skin Surface Electrodes – Needle Electrodes – Reference Electrodes – Digital Thermometer – Sphygmomanometer – Electronic Stethoscope | 10 |

| | | | |
|-----------|---------------------------------|--|-----------|
| II | Recorders and meters | ECG - EEG - EMG - - Cardio Tocography - Electro Oculography - Electro Retinography - PolySomnography- Spirometer-BloodFlow | 10 |
|-----------|---------------------------------|--|-----------|

| | | | |
|------------|--|---|-----------|
| | | Meter-Doppler-Audiometer | |
| III | Ts | TS.UpperEndoscope-LowerEndoscope- ENTEndoscope--Diathermy - Surgical Diathermy- MicroWaveDiathermy- MultiparaPatientMonitor. | 10 |
| IV | Intensive carets | PulseOximeter-BlockDiagram&Sensor – Ventilator – Cardiac Monitor - ECGHolder-Defibrillator-Pace maker : Implantable and External Pacemakers - Infant Warmer - Infant Incubator - BabyPhototherapy–Nebulizer. | 10 |
| V | Electricalsa fety ofmedicalin struments | Radiation Safety - Physiological EffectsDue to 50 Hertz Current Passage - MicroShock-MacroShock- ElectricalAccidentsinHospitals- DevicestoProtectAgainstElectricalHazar ds–SMPSinMedical Equipments. | 10 |

REFERENCEBOOKS:

1. BiomedicalInstrumentation&Measurements–AnandaNatarajan–PHIRS.275/-
2. BiomedicalInstrumentationAndMeasurements-LeslieCromwell-PHI-2ndEdition.
3. Bio-MedicalInstrumentation-Dr.M.Arumugam- AnuradhaAgencies-2ndEdition
4. HandbookOfBiomedicalInstrumentation- R.S.Khandpur–TMH.
5. MedicalInstrumentation,ApplicationAndDesign–JohnG.Webster-WEL-3rdEdition
6. ATextOfBookOfMedicalInstruments–Ananthi–NewAgeInternationalRs.275/-
7. Internet:AdditionalReferenceforallUnits.

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER
RVI
ELECTIVE III-
PAPER I GROUP C PCB DESIGN AND FABRICATION**

| | | | |
|----------|----------|----------|----------|
| L | T | P | C |
| 5 | 0 | 0 | 5 |

COURSE OBJECTIVES::

Understand the need for PCB and steps involved in PCB Design and Fabrication process. PCB (Printed Circuit Board) designing is an integral part of each electronics products and this program is designed to make students capable to design their own projects PCB upto industrial grade.

COURSE OUTCOMES: At the end of course, student will be able to:

| | |
|------------|--|
| CO1 | Appreciate the necessity and evolution of PCB, types and classes of PCB. |
| CO2 | Understand the steps involved in schematic, layout, fabrication and assembly process of PCB design. |
| CO3 | Familiarize Schematic and layout design flow using CAM Tools. |
| CO4 | This is a basic course for designing of PCB using software. Design (schematic and layout) and fabricate PCB for simple circuits. |
| CO5 | Understand the environmental concerns |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|-------------------|--|-----------------------------|
|-------------|-------------------|--|-----------------------------|

| | | | |
|----------|---|---|-----------|
| I | Quick start & components | PCB Introduction – Surface Mount and Through hole – Prototyping – Installing EAGLE Light Edition on Windows- Light Edition Limitations – Creating New Project – Drawing the Schematic – | 10 |
|----------|---|---|-----------|

| | | | |
|-----|--|---|----|
| | | Electrical Rule Check – Laying out the Board – U.S. Versus European Circuit Symbols – Resistors – Capacitors – Transistors & Diodes – IC'S – Connectors – Other Components – Buying Components – Paper PCB. | |
| II | Editingschematics & Pcb layout | The Anatomy of the Schematic Editor – The Command Toolbar – Nets – Buses – Worked Examples. PCBLAYOUT: Experimenting – Layers – The Command Toolbar – The Grid – Sound Meter Layout (Through Hole) – Sound Meter Layout (Surface Mount) – Manual Layout. | 10 |
| III | Pcb fabrication & pcb soldering | Gerber Files – Loading a CAM Job – Running a CAM Job – Measure Twice, Cut Once – Submitting a job to a PCB Service – Instructions – Photoetching – Milling PCBs – Toner Transfer. PCBSOLDERING: General Tools – Tools for Surface Mount Devices – Soldering Through Hole PCB's – SMD Hand Soldering – SMT with Hot Air Gun – Using a Reflow Oven. | 10 |
| IV | Commands & scripts | Commands – Scripts – User Language Programs – Creating Library – Copying a Device from Another Library – The Part Editor – Devices , Symbols , and Packages – Editing a Part – Creating New Part. | 10 |

| | | | |
|----------|-------------------------------|--|-----------|
| V | Environmental concerns | PollutionControlinPCBIndustry– PollutiongAgents–RecyclingofWater –RecoveryTechniques–AirPollution– | 10 |
|----------|-------------------------------|--|-----------|

| | | | |
|--|--|---|--|
| | | Recycling of PCB's – Environmental Standards–Safety Precautions– Toxic Chemicals. | |
|--|--|---|--|

REFERENCE BOOKS:

1. Make Your Own PCB's With EAGLE–Simon Monk–McGraw Hill (2014)
2. EAGLE MANUAL–Version V–Seventh Edition.
3. PCB Design Fabrication, Assembly and Testing–Dr. R.S. Khandpur–TMH.
4. PCB Design & Fabrication–Walter. C. Bosshart–TMH

B.SC. ELECTRONICS AND**COMMUNICATION SEMESTER****RVI****ELECTIVE III - PAPER II - GROUP****C PROGRAMMING USING VERILOG HDL (SIMPLE CONCEPTS ONLY)**

| | | | |
|----------|----------|----------|----------|
| L | T | P | C |
| 5 | 0 | 0 | 5 |

COURSE OBJECTIVES: Students will try to learn:

Understand the concepts of Verilog Language, Inspect how effectively ICs are embedded in package and assembled in PCBs for different application. Design and diagnosis of processors and I/O controllers used in VHDL.

COURSE OUTCOMES: At the end of course, student will be able to:

| | |
|------------|--|
| CO1 | Describe the basic language features of Verilog HDL and the role of HDL in digital logic design. |
| CO2 | Describe the basic concepts Verilog HDL |
| CO3 | Describe Verilog model for sequential circuits and test pattern generation. |
| CO4 | Design data flow modeling. |
| CO5 | Synthesize different types of behavioral modeling. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|---------------------------------|---|---------------------------------|
| I | Basic verilog topics | Overview – Design Flow – Hierarchical Modeling Concepts – Design Methodologies – Modules – Instances – Components of Simulation. | 10 |

| | | | |
|-----------|-----------------------|---|-----------|
| II | Basic concepts | Lexical Conventions–White Space– Comments–Operators– Number Specification–Strings–Identifier and Keywords–Data Types–Nets–Registers– | 10 |
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| | | | |
|------------|----------------------------|--|-----------|
| | | Vectors Arrays – Memories – Parameters – Strings – System Tasks and Compiler Directives. | |
| III | Modules and ports | Modules – Ports – Port Declaration – Port Connection Rules – Connecting Ports to External Signals – Gate Level Modeling – Gate Types. | 10 |
| IV | Data flow modeling | Continuous Assignment – Delays – Expression – Operators and Operands – Operator Types – Behavior Modeling – Structure Procedures – Procedural Assignments – Timing Controls. | 10 |
| V | Behavioral modeling | Conditional statements – Multiway Branching – Loops – Sequential and Parallel Blocks – Generate Blocks – Logic Synthesis with Verilog HDL – Impact of Logic Synthesis – Verilog HDL Synthesis – Synthesis Design Flow. | 10 |

Text Book:

1. Verilog HDL – A Guide to Digital Design and Synthesis – Samir Palnitkar – Pearson – II EDITION.

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER VI
ELECTIVE III - PAPER III - GROUP
CELECTRONIC DEFENSE SYSTEMS
(SIMPLE THEORY ONLY)**

| L | T | P | C |
|---|---|---|---|
| 5 | 0 | 0 | 5 |

COURSE OBJECTIVES: Students will try to learn:

Understand the concept of electronic warfare. and acquainted with the basic characteristics and requirements of electronic warfare receivers, the basic principles of electronic countermeasures, its types and classes of ECM and the parameters of radars, and radar systems pertaining to ECM,

COURSE OUTCOMES: At the end of course, student will be able to:

| | |
|------------|---|
| CO1 | Gain knowledge in the fundamental principle of Electronic defense system, weapon systems |
| CO2 | Analyse the functioning and interrelations of subsystems in an electronic warfare system |
| CO3 | Develop technical architecture of electronic intercept systems in preliminary system design level |
| CO4 | Develop basic simulation and analysis tools for the assessment of a given ECM. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|-------------------|--|-----------------------------|
| | | | |

| | | | |
|----------|--------------------------|---|-----------|
| I | Electronicdefense | Introduction – Systems in Use in theArmedForces– TheMainWeaponSystems– Objectives & OrganizationofElectronicDefense– OperationalObjectives - Information Operation &Warfare– NeedfortheStudyof WeaponSystems. | 10 |
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| | | | |
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| | | | |
| II | Weaponsystems | Artillery Systems – Missile Systems (Any3)–PassiveAntiradiationMissiles–StealthAircraft–CommunicationSystems–Information Operations. | 10 |
| III | Electronic interceptsystems | Introduction–RadarWarningReceivers–ElectronicSupportMeasures–OmnidirectionalAntennas –AntennasforDirectionFinding–DigitalReceivers–ElectronicIntelligentSystems–AdvancedPassive Location Techniques – InfraredInterceptSystems–Communications ESM&CommunicationIntelligence. | 10 |
| IV | Electroniccountermeasureystems | Introduction–OffBoardPassive&ActiveECMSystems – OnBoardPassive&ActiveECMSystems –ECM Techniques (Any 3) – InfraredCountermeasures–CommunicationsCountermeasures–Information Warfare. | 10 |
| V | Electronic counter-countermeasuresystems | Introduction–TrackingRadarCounter-Countermeasures–InfaredCounter-Countermeasures – Communications Counter – Countermeasures–NewElectronic DefenseArchitectures. | 10 |

TextBook:

1. IntroductionToElectronicDefenseSystems–FilippoNeri–NewAgeInternational–SecondEdition-Rs.395/-

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER
RVI
ELECTIVE III- PAPER IV-GROUP C**

**NANOELECTRONICS
(SIMPLE THEORY ONLY)**

| | | | |
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COURSE OBJECTIVES: Students will try to learn:

To introduce the nanoelectronics, nanodevices, spintronics and molecular electronics. Understand the diverse electronic device fabrication, describe the principle and the operation of nanoelectronic devices. In-depth technical knowledge in one or more areas of specialization.

COURSE OUTCOMES

| | |
|------------|--|
| CO1 | Ability to perform simple analysis of Nanoelectronic devices and calculate the density of states in Nanoelectronic devices. |
| CO2 | Ability to perform in-depth analysis of self-assembly in Nanoelectronic devices |
| CO3 | Nano Electronics and Nano Microfabrication course is designed to encompass all these aspects, viz., nano and micro regime design, simulation and fabrication and all types of IC's, microfluidics. |
| CO4 | It is expected that, after undergoing this course, the students will acquire both theoretical knowledge and practical skills in diverse upcoming areas of current technology. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|-------------------|--|---------------------------------|
| | | | |

| | | | |
|----------|--|--|-----------|
| I | Introduction & ethical issues | What is Nanotechnology-Advantages- Scope - Limits of Nanotechnology - Solutions cause Problems-Change causes | 10 |
|----------|--|--|-----------|

| | | | |
|------------|---|---|-----------|
| | | Problems - Clean, Decentralized Production causes Problems - Even Wealth & Leisure cause Problems - Changing Employment causes Problems - Frequently Asked Questions. | |
| II | Selfassembly | Bottom - Up Self Assembly - Top Bottom Assembly - Other Production Processes - MEMS Process - Deposition Processes - Lithography - Etching Processes. | 10 |
| III | Instrumentation techniques | AFM - SEM - TEM - Auger Electron Spectroscopy - LASER Induced Breakdown Spectroscopy. | 10 |
| IV | Nanoelectronics & carbon nanotubes | Triple Gate MOSFET & EJ-FET Properties - Carbon Nanotubes - Fabrication Methods - CNT Based Biosensors and Advantages - Properties of CNT - Fuel cells & Nanotech. | 10 |
| V | Nano -bio | Nanotechnology in Medicine - Working Outside TISSUES - Working Within Tissues. Applications: Killing Cancer Cells - Providing Oxygen - Artificial Mitochondria. | 10 |

REFERENCE BOOKS:

1. NanoTechnology - A Future Technology With Visions - Appin LABS - BPB - Rs.270/-
2. Nano: The Essentials "Understanding Nano Science & Nano Technology" - T Pradeep - TMH

B.SC.ELECTRONICSANDCOMMUNICATION**SEMESTERVI**

| | | | |
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| L | T | P | C |
| 2 | 0 | 0 | 2 |

SKILLED BASED ELECTIVE**COURSESBE CVI-LIFE****DEVELOPMENTSKILLS****COURSEOBJECTIVES:**

This course is designed to enhance the employability and maximize the potential of the students by introducing them to the principles that underly personal and professional success, and help them acquire the skills needed to apply these principles in their lives and careers. Prerequisite

COURSEOUTCOMES: After completion of this course, student able to

| | |
|------------|--|
| CO1 | Define and Identify different life skills required in personal and professional life |
| CO2 | Develop an awareness of the self and apply well-defined techniques to cope with emotions and stress. |
| CO3 | Develop an Preparing for a Job Interview |
| CO4 | Understand the basics of teamwork and leadership |
| CO5 | the basic mechanics of effective communication. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|----------------------|---|-----------------------------|
| I | Self analysis | Self Analysis and Self Concept – Understanding Self: Attitude, Aptitude and Self Esteem – Assertiveness – Confidence Building – Motivation: Concept, Theories and Importance. | 05 |

| | | | |
|-----------|------------------------|---|----|
| II | Selfdevelopment | Introduction – Goal Setting : Concept, Setting SmartGoals– Emotion:Concept,Types,EmotioinalIntelligence– Creativity:ConceptandFactors EnhancingCreativity–StressManagement–Health | 05 |
|-----------|------------------------|---|----|

| | | | |
|------------|----------------------------|---|----|
| | | Management : Importance, Dietary Guidelines and Exercises – Time Management: Importance and process. | |
| III | Looking for a job | Identifying Different Sources Announcing Job Vacancies – Skimming, Scanning and Reading Advertisements in Detail – Writing Effective CVs – Covering Letters that Accompany CVs – Techniques of Writing Job Application Letters / Covering Letters – Preparing for a Job Interview. | 05 |
| IV | Non – verbal skills | Graphic Communication – Non Verbal Communication – Aspects of body Language – Formal Written Communication – Memos (Memoranda) – E-mails – Netiquette – Business Correspondents. | 05 |
| V | Telephone skills | Understanding Telephone Communication – Types of calls – Handling Calls – Leaving a Message – Making Requests – Asking for and Giving Information – Giving Instructions – Agreeing and Disagreeing – Making or Changing Appointments – Reminding – Making Complaints and Handling Complaints – Telephone Etiquette. | 05 |

REFERENCE BOOKS:

1. Development Of Life Skills And Professional Practice – Shalini Verma – Vikas Publishing House Pvt Ltd., Rs. 200/- (Units: I & II)
2. Development Of Life Skills – II – Shalini Verma – Vikas Publishing House Pvt Ltd., Rs. 225/- (Units: III & IV)
3. A Course In Communication Skills - P. Kiranmai Dutt – Cambridge University Press India Pvt. Ltd., Foundation Books, Cambridge House, 4381/4, Ansari Road, Daryaganj, New Delhi-110002. (Unit: V)

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER V & VI
CORE PRACTICAL IV
IC'S & COMMUNICATION LAB**

| L | T | P | C |
|---|---|---|---|
| 2 | 2 | 2 | 4 |

(ANY HARDWARE BASED SIMULATION TOOL MAY ALSO BE USED)

(Any 22 Experiments)

COURSE OBJECTIVES:

The main aim of this lab is to teach the linear and non-linear applications of operational amplifiers (741). Students are made familiar with theory and applications of 555 timers. Students are made to Design combinational logic circuits using digital ICs & To acquire the basic knowledge of special function ICs

COURSE OUTCOMES:

| | |
|------------|--|
| CO1 | Design and analyse the various digital circuits. |
| CO2 | Design and analyse the various linear & non-linear applications of op-amp. |
| CO3 | Design and analyse oscillators and multivibrator circuits using op-amp & Timers. |
| CO4 | Design and analyse the various communication applications of op-amp. |
| CO5 | Practice the basic mechanics of conversions. |

Syllabus

1. NAND GATE Implementation Using TTL.
2. NOT/NAND/NOR GATE Implementation Using IIL/CMOS.
3. OR/NOR GATE Implementation Using ECL.
4. Inverting/Non-Inverting Amplifier Using Op-Amp
5. Inverting/Non-Inverting Adder Using Op-Amp.
6. Subtractor Using Op-Amp.

7. Multiplier/DividerUsingOp-Amp.
8. Differentiator/IntegratorUsingOp-Amp.
9. LowPass/HighPassFilterUsingOp-Amp.
10. BandPass/BandRejectFilterUsingOp-Amp.
11. Voltage to Current Converter (Grounded load).
12. Current to Voltage Converter.
13. Solving of Simultaneous Equations Using Op-Amp.
14. Square Wave Generation Using Op-Amp
15. Triangular Wave Generation Using Op-Amp
16. Phase Shift Oscillator Using Op-Amp.
17. Wein Bridge Oscillator Using Op-Amp.
18. Amplitude Modulation & Demodulation.
19. Pulse Amplitude Modulation & Demodulation.
20. Pulse Width Modulation & Demodulation.
21. Astable Multivibrator Using 555 IC
22. Monostable Multivibrator Using 555 IC Triangular
23. Waveform Generation Using 555 IC
24. Voltage Controlled Oscillator Using 555 IC
25. Schmitt Trigger Using 555 IC
26. Frequency Multiplication Using 565 IC
27. Binary Weighted Resistor Type of DAC
28. R-2R Ladder Type of DAC.
29. ADC Using ADC0809
30. DAC Using DAC0800

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER V & VI
CORE PRACTICAL V-Group D**

| L | T | P | C |
|---|---|---|---|
| 2 | 2 | 2 | 4 |

8051 MICROCONTROLLER & INTERFACING LAB

(ANY EMBEDDED BASED SIMULATION TOOL MAY ALSO BE USED)

(Any 17 Experiments)

Course Objectives

To expose students to the operation of typical Microcontroller (8051) **simulation tool/** trainer kit. solve different problems by developing different programs & to develop the quality of assessing and analyzing the obtained data.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|--|
| CO1 | Identify relevant information to supplement to the Microcontroller (8051). |
| CO2 | Set up programming strategies and select proper mnemonics and run their program on the training boards. |
| CO3 | Practiced different types of programming keeping in mind technical issues and evaluate possible causes of discrepancy 2 experimental observations in comparison. |
| CO4 | Develop testing and experimental procedures on Microcontroller (8051) analyze their operation under different cases. |
| CO5 | Prepare professional quality textual and computational results, incorporating accepted data analysis and synthesis methods, simulation software, and word-processing tools. |

Syllabus

1. Addition of Two; 8 Bit Numbers & Subtraction of Two; 8 Bit Numbers
2. Multiplication of Two; 8 Bit Numbers
3. Division of Two; 8 Bit Numbers
4. BCD Addition

5. BCDSubtraction
6. FILL
7. BLOCKMOVE
8. 1's&2'sComplementofa16BitNumber.
9. Smallest/Largestof;NNumbers.
10. Ascending/DescendingOrderof;NNumbers.
11. SumofN;8BitNumbers.
12. MultiByteAddition.
13. 3-DigitDecimaltoTwo-digitHexadecimalConversion.
14. Two-digitHexadecimalto3-digitDecimalConversion.
15. ADCInterface.
16. DACInterface.
17. WaveformGenerationUsingDAC.
18. StepperMotorInterface.
19. InterfaceWithSwitches&InterfacewithLED's
20. HexKeyboardInterface.
21. InterfaceWithSingleSevenSegmentDisplay.
22. InterfacingWithMultipleSevenSegmentDisplays.
23. InterfaceWithLCD's.
24. InterfacingWithSolidStateRelay.
25. MovingDisplay.
26. BlinkingDisplay.
27. DigitalClock.
28. TrafficLightControl.

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER V & VI**

CORE PRACTICAL V-Group D

16F877 MICROCONTROLLER & INTERFACING LAB

(ANY EMBEDDED BASED SIMULATION TOOL MAY ALSO BE USED)

(Any 17 Experiments)

| L | T | P | C |
|---|---|---|---|
| 2 | 2 | 2 | 4 |

Course Objectives

To expose students to the operation of typical Microcontroller (16F877) simulation tool/ trainer kit. solve different problems by developing different programs & to develop the quality of assessing and analyzing the obtained data.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|--|
| CO1 | Identify relevant information to supplement to the Microcontroller (16F877). |
| CO2 | Set up programming strategies and select proper mnemonics and run their program on the training boards. |
| CO3 | Practiced different types of programming keeping in mind technical issues and evaluate possible causes of discrepancy in practical experimental observations in comparison. |
| CO4 | Develop testing and experimental procedures on Microcontroller (16F877) analyze their operation under different cases. |
| CO5 | Prepare professional quality textual and computational results, incorporating accepted data analysis and synthesis methods, simulation software, and word-processing tools. |

Syllabus

1. Addition of Two; 8 Bit Numbers & Subtraction of Two; 8 Bit Numbers
2. Multiplication of Two; 8 Bit Numbers
3. Division of Two; 8 Bit Numbers

4. BCDAddition
5. BCDSubtraction
6. FILL
7. BLOCKMOVE
8. 1's&2'sComplementofa16BitNumber.
9. Smallest/Largestof;NNumbers.
10. Ascending/DescendingOrderof;NNumbers.
11. SumofN;8BitNumbers.
12. MultiByteAddition.
13. 3-DigitDecimaltoTwo-digitHexadecimalConversion.
14. Two-digitHexadecimalto3-digitDecimalConversion.
15. ADCInterface.
16. DACInterface.
17. WaveformGenerationUsingDAC.
18. StepperMotorInterface.
19. InterfaceWithSwitches&InterfacewithLED's
20. HexKeyboardInterface.
21. InterfaceWithSingleSevenSegmentDisplay.
22. InterfacingWithMultipleSevenSegmentDisplays.
23. InterfaceWithLCD's.
24. InterfacingWithSolidStateRelay.
25. MovingDisplay.
26. BlinkingDisplay.
27. DigitalClock.
28. TrafficLightControl.

**B.SC. ELECTRONICS AND
COMMUNICATION SEMESTER V
& VI
CORE PRACTICAL**

| L | T | P | C |
|---|---|---|---|
| 2 | 2 | 2 | 4 |

**VPLC PROGRAMMING LAB
B**

(ANY EMBEDDED BASED SIMULATION TOOL MAY ALSO BE USED)

(Any 17 Experiments)

Course Objectives

To explain the basic concepts of a Programmable Logic Controller. Students will be able to state basic PLC terminology and their meanings. Students will be able to explain and apply the concept of electrical ladder logic, its history, and its relationship to programmed PLC instruction.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|---|
| CO1 | Students will be able to describe typical components of a Programmable Logic Controller. |
| CO2 | Students will be able to explain and apply the concept of electrical ladder logic, its history, and its relationship to programmed PLC instruction. |
| CO3 | Students will be able to explain the concept of basic digital electronics and data manipulation. |
| CO4 | Students will be able to use timer, counter, and other intermediate programming functions |
| CO5 | Students will be able to use timer, counter, and other intermediate programming functions |

1. StudyofPLCSymbols
2. StudyofVariousLogicExecutioninLadderDiagram.

3. Writing of Ladder Logic for Different Statements.
4. Ladder Diagram Development for Different Types of Logic Gates using Suitable Software
5. PLC Input-Output Wiring Methods
6. Operating Simple Loads using Relays, Switches and Pushbuttons
7. Different Applications of Push Buttons
8. Programming the PLC via Ladder Logic
9. Working of Different Types of Timers
10. Study & Implement; ON delay timer in PLC
11. Study & implement; OFF delay timer in PLC
12. Working of Different Types of Counters
13. Study & Implementation of Up Counter in PLC Programming.
14. Study & Implementation of Down Counter in PLC Programming.
15. Interlocking
16. Sequencer
17. Sequential Operation of On/Off of a Set of Lights
18. Forward and Reverse Direction Control of Motors
19. Latching and Unlatching of motor
20. Prepare the Physical and Programmed Ladder Diagram for the Control Problem shown below & Implement the same.
21. PLC Programming for Bottle Filling Plant.
22. Procedure for Producing a Ladder Logic Diagram for Car Parking Simulation
23. Position Control for Satellite Dish DCMotors
24. Starting Three Phase Induction Motors via Star-Delta Starter
25. Automatic Indication of Water Tank Level
26. Traffic Lights Indication

B.SC.ELECTRONICSAND COMMUNICATION
CIRCUITIMPLEMENTATIONUSINGARDUINOUNOBOARDSCOMMONFO
RALL PRACTICALS UNDERGROUPD

ANYFIVE

| L | T | P | C |
|---|---|---|---|
| 2 | 2 | 2 | 4 |

CourseObjectives

To provide knowledge of different Smart System applications. To familiarize students with Arduino as IDE, programming language & platform. To provide knowledge of Arduino boards and basic components. Develop skills to design and implement various smart system application.

COURSEOUTCOMES: After completion of this course, student able to

| | |
|------------|---|
| CO1 | Familiar with Arduino environment and its applications. |
| CO2 | Able to understand Arduino programming with C++. |
| CO3 | Able to Design Smart systems applications. |
| CO4 | Learn and understand about any new IDE, compiler, and MCU chip in Arduino |

Syllabus

1. LED Control Through Push Buttons
2. Photo Resistor as Light Intensity Detector
3. DC Motor Direction Control using L293D
4. DC Motor Speed Control using L293D
5. Displaying Text on LCD Display
6. Scrolling Text on LCD Display
7. Interfacing 4x3 Keypad and LCD
8. Displaying Alphanumeric Character on Dot Matrix Display
9. Display of Numeric Characters using 7 Segment Display.

10. UnipolarStepperMotorSpeedControlUsingULN2003A
11. LEDControlThroughSerialCommunication
12. LEDControlusingInfrared
13. DisplayingCurrentDateandTimeon16x2LCD
14. SwitchingControlofACLoadusingTriac
15. VoltageRegulationAcrossACLoadusingTriac
16. SwitchingControlofACLoadThroughSMSTextMessage

REFERENCEBOOK

ArduinoProjectsforEngineers-NeerparajRai–BPBPublications –Rs.297/-

**B.SC. ELECTRONICS AND
COMMUNICATION APPLIED ELECTRONICS
CS-I (ALLIED)**

(For B.ScCS, BCA, B.ScIT, & B.ScPHY)

I/II YEAR-I/II SEMESTER

| | | | |
|----------|----------|----------|----------|
| L | T | P | C |
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COURSE OBJECTIVES:

- To study about the fundamentals of semiconductor.
- To gain the basic knowledge of electronic components and the function of power supply.
- To understand the operation of oscillator, amplifier and filters.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|---|
| CO1 | Understand the principle of semiconductor devices. |
| CO2 | Have a thorough understanding of the fundamental concepts of electronic components |
| CO3 | Analyze and design various conceptual theory of the power supply. |
| CO4 | Distinguish between the sinusoidal and non-sinusoidal waveforms. |
| CO5 | Acquire the knowledge on the types and principles of oscillators, amplifiers and filters. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|-------------------|--|-----------------------------|
|-------------|-------------------|--|-----------------------------|

| | | | |
|----------|-----------------------------|--|----|
| I | Semiconductor Theory | Intrinsic Semiconductor–Extrinsic Semiconductor- Theory of PN Junction diode-Zener Diode–Avalanche Breakdown - Zener Break down - Operation of PNP & NPN Transistor-CB, CE, CC Configuration and Characteristics- Transistor as an Amplifier. | 10 |
|----------|-----------------------------|--|----|

| | | | |
|------------|--|---|----|
| II | Resistors, Capacitors & Inductors | Resistors, Capacitors & Inductors in Series and Parallel - Factors governing the Resistance of a Resistor, Capacitor & Inductor - Colour Coding of Resistors - Various Other Passive & Active Devices – Ohm's & Kirchoff Laws - Series Circuits – Parallel Circuits - Series & Parallel Circuits - Open Circuit - Short Circuit | 10 |
| III | Power Supply's | Half Wave Rectifier - Full Wave Rectifier - Bridge Rectifier - Capacitor Filter - Fixed IC Regulated Power Supply using 78XX - Dual IC Regulated Power Supply using 78XX & 79XX. | 10 |
| IV | Waveforms | Sinusoidal Waveform - Non-Sinusoidal Waveform - Peak Value - Peak to Peak Value - Average Value – RMS Value – Period & Frequency Measurement – Use of Digital Multimeter – Use of CRO. | 10 |
| V | Oscillators, Amplifiers & Filters | Barkhausen Criterion – Sinusoidal Oscillators : Hartley, Phase Shift & Crystal Oscillator. Non Sinusoidal Oscillators : Astable, Monostable & UJT as a Sawtooth Oscillator. Amplifiers: RCC Coupled & Transformer Coupled Amplifiers. Filters: Low Pass, High Pass, Band Pass & Band Reject Filters. | 10 |

Books for Study

1. Electronic Devices & Circuits - S. Salivahanan - TMH - II Edition
2. Circuits & Networks - Sudhakar - TMH - 4th Edition..

**B.SC. ELECTRONICS AND
COMMUNICATION APPLIED ELECTRONICS
CS-II (ALLIED)**

(For B.Sc CS, BCA, B.Sc IT, & B.Sc PHY)

I/II YEAR-II/IV SEMESTER

| | | | |
|----------|----------|----------|----------|
| L | T | P | C |
| 4 | | | 4 |

COURSE OBJECTIVES:

- To study about the fundamental concepts of transducers and op-amps.
- To gain the basic knowledge of IC and PCB fabrication process.
- To understand the fundamentals of communication principle, modulation techniques, transmission and reception.

COURSE OUTCOMES: After completion of this course, student able to

| | |
|------------|---|
| CO1 | Understand the principle and types of the various transducers. |
| CO2 | analyze and design basic op-amp circuits, non-linear circuits, comparator, signal generators, and oscillator. |
| CO3 | acquire qualitative knowledge about the fabrication process of integrated circuit & PCB fabrication |
| CO4 | Comprehend the working principle of the communication systems. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Program mespecific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|--------------------|---|-----------------------------|
| I | Transducers | Resistive Transducers – Inductive Transducers - Capacitive Transducers - Piezo Electric Transducer - Thermo Electric Transducers – Temperature Transducers – Instrumentation Amplifier – Microphones & Loud Speakers. | 10 |

| | | | |
|------------|--------------------------------|---|----|
| II | Op–Amp’s | Introduction - The ideal OP-AMP - OP-Amp stages - OP-Amp parameters- Inverting & Non Inverting Amplifier- Adder- Subtractor- Multiplier- Divider- Integrator - Differentiator - V to I Converter - I to V Converter. | 10 |
| III | IC Fabrication Process | Basic Planar Process- Fabrication of a Typical Circuit - Active and Passive Components- Fabrication of FET, MOSFET & CMOS- Thin & Thick Film Technology. | 10 |
| IV | PCB Fabrication Process | PCB Types – Layout & General Rules – Design Rules For Digital Circuit PCB's – Artwork – Properties & Types of Copper Clad Laminates – Photo Printing – Screen Printing – Types of Etchants – Manual Routing – Auto Routing – Design Rule Check. | 10 |
| V | Communication Systems | Need For Modulation – Amplitude Modulation & Detection - Frequency Modulation & Detection – AM Transmitter - AM Receiver – FM Transmitter - FM Receiver – Modulation & Detection of: PAM – PPM – PWM – PCM. | 10 |

Books for Study

1. Electronic Devices & Circuits - S. Salivahanan - TMH - II Edition
2. PCB Design - Walter C. Bosshart - TMH
3. Electronic Communication Systems - Kennedy - TMH - IV Edition.

B.SC.ELECTRONICSANDCOMMUNICATION**APPLIEDELECTRONICSLAB(ALLIED)**

(ForB.ScCS, BCA,B.ScIT,&B.ScPHY)

I/IIYEAR-II/IVSEMESTER

(ANYHARDWAREBASEDSIMULATION TOOLMAYALSOBEUSED)

(Any18Experiments)

| L | T | P | C |
|---|---|---|---|
| 2 | 1 | 1 | 2 |

CourseObjectives

To introduce electric circuits and its analysis & to impart knowledge on solving circuits using network theorems, digital circuits & electronic circuits.

COURSEOUTCOMES: After completion of this course, student able to

| | |
|------------|--|
| CO1 | Analyse the characteristics of PN Junction diode, transistors |
| CO2 | To impart knowledge on solving circuits using network theorems |
| CO3 | Determine frequency response of RC circuits and simulate series, parallel resonant circuits. |
| CO4 | Learn principal of digital Electronics. |
| CO5 | Demonstrate the applications of op-amp & timer |

Syllabus

1. PN Junction Diode Characteristics
2. CE Input Characteristics
3. CE Output Characteristics
4. Ohm's Law
5. Kirchoff's Current Law.
6. Kirchoff's Voltage Law.

7. Resistors in Series & Parallel
8. Capacitors in Series & Parallel
9. Measurement of Amplitude & Frequency Using CRO.
10. NAND as a UNIVERSAL GATE (AND/OR)
11. NOR as a UNIVERSAL GATE (AND/OR)
12. Verification of De Morgan's Theorem.
13. Truth Table Verification of BASIC Gates (Any one gate)
14. Half Adder/Full Adder
15. Half Subtractor/Full Subtractor.
16. Encoder Using 74147 IC
17. Decoder Using 7442 IC
18. Multiplexer Using 74153 IC
19. Demultiplexer Using 74155 IC
20. Clock Generation Using NAND/NOR GATE
21. Full Wave Rectifier With Capacitor Filter.
22. Dual IC Regulated Power Supply (78XX & 79XX).
23. Inverting Adder/Non-Inverting Adder Using Op-Amp
24. Subtractor Using Op-Amp
25. Low Pass Filter/High Pass Filter.

B.SC. ELECTRONICS AND**COMMUNICATIONELECTRONICS–****I(ALLIED)**

**For B.Sc[MATHS, STATISTICS, MATHS(CA),
BIO CHEMISTRY, MICROBIOLOGY, BIO-**

| | | | |
|----------|----------|----------|----------|
| L | T | P | C |
| 4 | | | 4 |

TECH|I/II YEAR-I/II SEMESTER**COURSE OBJECTIVES::**

To understand operation of semiconductor devices., DC analysis and AC models of semiconductor devices. To verify the theoretical concepts through laboratory and simulation experiments..

COURSE OUTCOMES: At the end of course, student will be able to:

| | |
|------------|--|
| CO1 | Understand the current voltage characteristics of semiconductor devices, |
| CO2 | Analyze dc circuits and relate ac models of semiconductor devices with their physical operation, |
| CO3 | Design and analyze of electronic circuits Laws |
| CO4 | Evaluate frequency response to understand behavior of waveforms. |

Syllabus

| Unit | Unit Title | Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4) | Hours of Instruction |
|-------------|-------------------|--|---------------------------------|
|-------------|-------------------|--|---------------------------------|

| | | | |
|----------|-----------------------------|---|----|
| I | Semiconductor theory | Intrinsic Semiconductor– Extrinsic Semiconductor– Theory of PN Junction Diode– Zener Diode–Avalanche Breakdown– Zener Break Down– Operation of PNP & NPN Transistor– CB, CE, CC Configuration and Characteristics– Transistor as an Amplifier. | 10 |
|----------|-----------------------------|---|----|

| | | | |
|-----|---|---|----|
| II | Resistors , capacitors&inductors | Resistors,Capacitors&InductorsinSeries and Parallel - Factors GoverningResistance,Capacitance&Inductance-ColourCodingofResistors-EnergyStoredinaCapacitor-EnergyStoredinanInductor-VariousOtherPassive&ActiveDevices. | 10 |
| III | Circuitlaws | Ohm's Law - Kirchoff's Current Law - Kirchoff'sVoltageLaw-VoltageDivision-CurrentDivision-SeriesCircuits - Parallel Circuits - Series &ParallelCircuits-OpenCircuit-Short Circuit. | 10 |
| IV | Waveforms | Sinusoidal Waveform - Non-SinusoidalWaveforms-PeakValue-PeaktoPeakValue-AverageValue-RMSValue-Period&FrequencyMeasurement | 10 |
| V | Powersupply | HalfWaveRectifier-FullWaveRectifier - Bridge Rectifier - CapacitorFilter-FixedICRegulatedPowerSupplyusing78XX-DualICRegulatedPowerSupplyusing78XX &79XX. | 10 |

REFERENCEBOOKS:

1. ElectronicDevices&Circuits-S.Salivahanan -TMH-IIEdition
2. Circuits&Networks-Sudhakar-TMH-4thEdition.

**B.SC. ELECTRONICS AND
COMMUNICATION ELECTRONICS-II
(ALLIED)**

| L | T | P | C |
|---|---|---|---|
| 4 | | | 4 |

For B.Sc [MATHS, STATISTICS, MATHS
(CA), BIOCHEMISTRY, MICROBIOLOGY, BIO-
TECH]

I/II YEAR-II/IV SEMESTER

COURSE OBJECTIVES:

- To acquire the basic knowledge of digital logic levels and application of knowledge to understand digital electronic circuits.
- To prepare students to perform the analysis and design of various digital electronic circuits.
- To learn the design process of registers, counters and conversion of analog to digital conversion and vice-versa.

COURSE OUTCOMES: At the end of course, student will be able to:

| | |
|------------|--|
| CO1 | Examine the structure of various number systems and its application in digital design. |
| CO2 | Have a thorough understanding of the fundamental concepts and minimization techniques used in digital electronics. |
| CO3 | Understand various Boolean algebra and its applications |
| CO4 | Understand various combinational logic circuits and its applications |
| CO5 | Analyze and design various Electronic circuits and its applications |

Syllabus

| Unit | Unit Title | Intended learning Chapters(Programme specific qualification attributes K1,K2,K3,K4) | Hours of Instructi on |
|-------------|----------------------|--|--------------------------------------|
| I | Numbersystems | Introduction-BinaryNumberSystem- | 10 |

| | | | |
|------------|-------------------------------|--|----|
| | | OctalNumberSystem-DecimalNumber System - Hexadecimal NmberSystem-ConversionFromOneSystem toAnother. | |
| II | Binaryrules | Binary Addition - Binary Subtraction - BinaryMultiplication-BinaryDivision -1's&2'sComplementSubtraction-9's&10'sComplementSubtraction. | 10 |
| III | Booleanalgebra | Logic GATES - NAND as aUNIVERSALGATE-NORasa UNIVERSAL GATE - Basis Laws ofBooleanAlgebra-PrincipleofDuality –DeMorgan'sTheorem. | 10 |
| IV | Combinational elements | Half Adder - Full Adder - HalfSubtractor - Full Subtractor - Encoder -Decoder-Multiplexer–Demultiplexer. | 10 |
| V | Electroniccircuits | Barkhausen Criterion – SinusoidalOscillators (Hartley , Phase Shift&Crystal Oscillator) - Amplitude &Frequency : Modulation & Detection –Amplifiers : RC Coupled &Transformer Coupled . Filters : LowPass, High Pass , Band Pass & BandReject Filters – Op-Amp's & TheirApplications (Addition , Subtraction,Multiplication ,Division, DifferentiationandIntegration). | 10 |

REFERENCEBOOKS:

1. DigitalCircuits&Design–Salivahanan-VikasPub-IIIEdition.
2. ElectronicDevices&Circuits-S.Salivahanan -TMH-IIIEdition

B.SC.ELECTRONICSANDCOMMUNICATION

ELECTRONICSLAB(ALLIED)

**For B.Sc [MATHS,STATISTICS,MATHS
(CA),BIOCHEMISTRY,MICROBIOLOGY,BIO-
TECH]**

I/IIYEAR-II/IVSEMESTER

| L | T | P | C |
|---|---|---|---|
| 2 | 1 | 1 | 2 |

(ANYHARDWAREBASEDSIMULATION TOOLMAYALSOBEUSED)

(Any18Experiments)

CourseObjectives

To introduce electric circuits and its analysis & to impart knowledge on solving circuits using network theorems, digital circuits & electronic circuits.

COURSEOUTCOMES: After completion of this course, student able to

| | |
|------------|--|
| C01 | Analyse the characteristics of PN Junction diode, transistors |
| C02 | To impart knowledge on solving circuits using network theorems |
| C03 | Determine frequency response of RC circuits and simulate series, parallel resonant circuits. |
| C04 | Learn principal of digital Electronics. |
| C05 | Demonstrate the applications of op-amp & timer |

Syllabus

1. PN Junction Diode Characteristics
2. CE Input Characteristics
3. CE Output Characteristics
4. Ohm's Law
5. Kirchoff's Current Law.

6. Kirchoff's Voltage Law.

7. Resistors in Series & Parallel
8. Capacitors in Series & Parallel
9. Measurement of Amplitude & Frequency Using CRO.
10. NAND as a UNIVERSAL GATE (AND/OR)
11. NOR as a UNIVERSAL GATE (AND/OR)
12. Verification of De Morgan's Theorem.
13. Truth Table Verification of BASIC Gates (Any one gate)
14. Half Adder/Full Adder
15. Half Subtractor/Full Subtractor.
16. Encoder Using 74147 IC
17. Decoder Using 7442 IC
18. Multiplexer Using 74153 IC
19. Demultiplexer Using 74155 IC
20. Clock Generation Using NAND/NOR GATE
21. Full Wave Rectifier With Capacitor Filter.
22. Dual IC Regulated Power Supply (78XX & 79XX).
23. Inverting Adder/Non-Inverting Adder Using Op-Amp
24. Subtractor Using Op-Amp
25. Low Pass Filter/High Pass Filter.

PROFESSIONAL ENGLISH- I

https://drive.google.com/file/d/1-edgjNpnnMIapLxZyqVH9z3jugy53CI/view?usp=drive_web

PROFESSIONAL ENGLISH-II

https://mail.google.com/mail/u/0?ui=2&ik=6b5209cdf&attid=0.1&permmsgid=msg-a:r351488560116746404&th=17921a031456e9df&view=att&disp=inline&realattid=f_ko3v5ewj0



PERIYAR UNIVERSITY

SALEM - 636011, TAMIL NADU, INDIA

NAAC A Grade - State University - NIRF Rank 68

THE REGISTRAR

PU/AD-1/BOS MEETING/ B.Sc.E& C /2020

Date: 30-01-2020

BOARD OF STUDIES - MEETING NOTICE

I am, by direction, to inform that the meeting of the Board of Studies in **B.Sc. Electronics and Communication -UG (Affiliated Colleges)** is scheduled to be held on **12-02-2020 at 10.00 a.m.** at Food Court, Periyar University, Salem-11.

AGENDA

1. To frame the syllabus for **B.Sc. Electronics and Communication** from the academic year 2020-2021 and thereafter.
2. Any other item.

I request you to make it convenient to attend the above meeting on 12-02-2020. TA & DA will be paid as per the University norms.(Remuneration will be paid through ECS only)

K. N. N. N.
REGISTRAR i/c 30.1.2020

To:

| | | |
|----|---|----------|
| 1. | Ms.S.Vennila Assistant Professor Dept. of Elec. & Communication, Government Arts College, Dharmapuri-636705. Ph:9952626888. | Chairman |
| 2. | Thiru L.Dhatchinamurthy Assistant Professor, Dept. of Elec. & Communication,K.S.R College of Arts & Science, K.S.R. Kalvi Nagar,Tiruchengode - 637209. Ph:9894588233. | Member |
| 3. | Thiru T.Suresh Assistant Professor & Head, Dept. of Elec. & Communication, Salem Sowdeswari College, Kondalampatti, Salem - 636010. Ph:9245519021. | Member |
| 4. | Thiru S.Karthikeyan Assistant Professor, Dept. of Elec. & Communication, K.S.R. College of Arts Science, K.S.R. Kalvi Nagar, Thiruchengode - 637209. Namakkal Dt. Ph:9865650753. | Member |

| | | |
|-----|---|---------------------|
| 5. | Dr.R.Ramesh Assistant Professor, Department of Physics Periyar University, Salem-11 Ph:9790177663. | Member |
| 6. | Dr.J.Kalyanasundar Assistant Professor, Department of Physics Periyar University, Salem - 636011. Ph:9965432165. | University Nominee |
| 7. | Dr.R.S.Sabeenian, Professor & HOD/ECE, R&D Head - SONA SIPRO, Department of Electronics & Communication Engineering, <u>Sona College of Technology</u> , Salem - 636 005, India. Ph:9894859444. | External Member |
| 8. | Mr. Prakash Manuel Joe J Associate Professor, Dept. of Electronics, PSG College of Arts and Science is a College, Avinashi Rd, PSG CAS, Civil Aerodrome Post, Peelamedu, Coimbatore, Tamil Nadu 641014. | External Member |
| 9. | Thiru Gopalakrishnan Manickam Technology Lead, Infosys Limited, Mahindra World City, Chengalpet, Chennai-603004. Mobile:9842448433. | Industrial Personal |
| 10. | Thiru S.Kannan Assistant Professor & Head, Dept. of Electronics & Communication, Muthayammal College of Arts and Science, Rasipuram, Namakkal-637408. Mobile:9943734799. | Alumni |

Copy to:1. P.A to Vice-Chancellor
2. P.A to Registrar
3. The Librarian, Library, Periyar University.